

PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

EN/LZT 146 302 R6D Apr 2007

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Key Features

- Industry standard MacroDens™ footprint
47.8 x 28.1 x max height 8.0 mm (1.88 x 1.11 x max height 0.32 in.)
- Identical electrical design as the PKF series products
- High efficiency, typ. 83 % at 5.0 Vout full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 7.2 million hours predicted MTBF at 40°C ambient temperature



General Characteristics

- Suited for narrow board pitch applications (15 mm/0.6 in)
- Over current protection
- Soft start
- Remote control
- Output voltage adjust function
- Input voltage adjust function
- Highly automated manufacturing to ensure highest quality
- ISO 9001/14001 certified supplier

Safety Approvals



Design for Environment



Meets requirements in high-temperature lead-free soldering processes.

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General Information

Ordering Information

See Contents for individual product ordering numbers.

Option	Suffix	Ordering No.
Through hole pin	P	PKR 4310 PI
SMD pin	S	PKR 4310 SI

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses one method, Telcordia SR332.

Predicted MTBF for the series is:

- 7.2 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Ericsson Power Modules products include:

- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

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Safety Specification

General information

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 "*Safety of information technology equipment*".

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.

In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).

Leakage current is less than 1 μ A at nominal input voltage.

24 V DC systems

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems

If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

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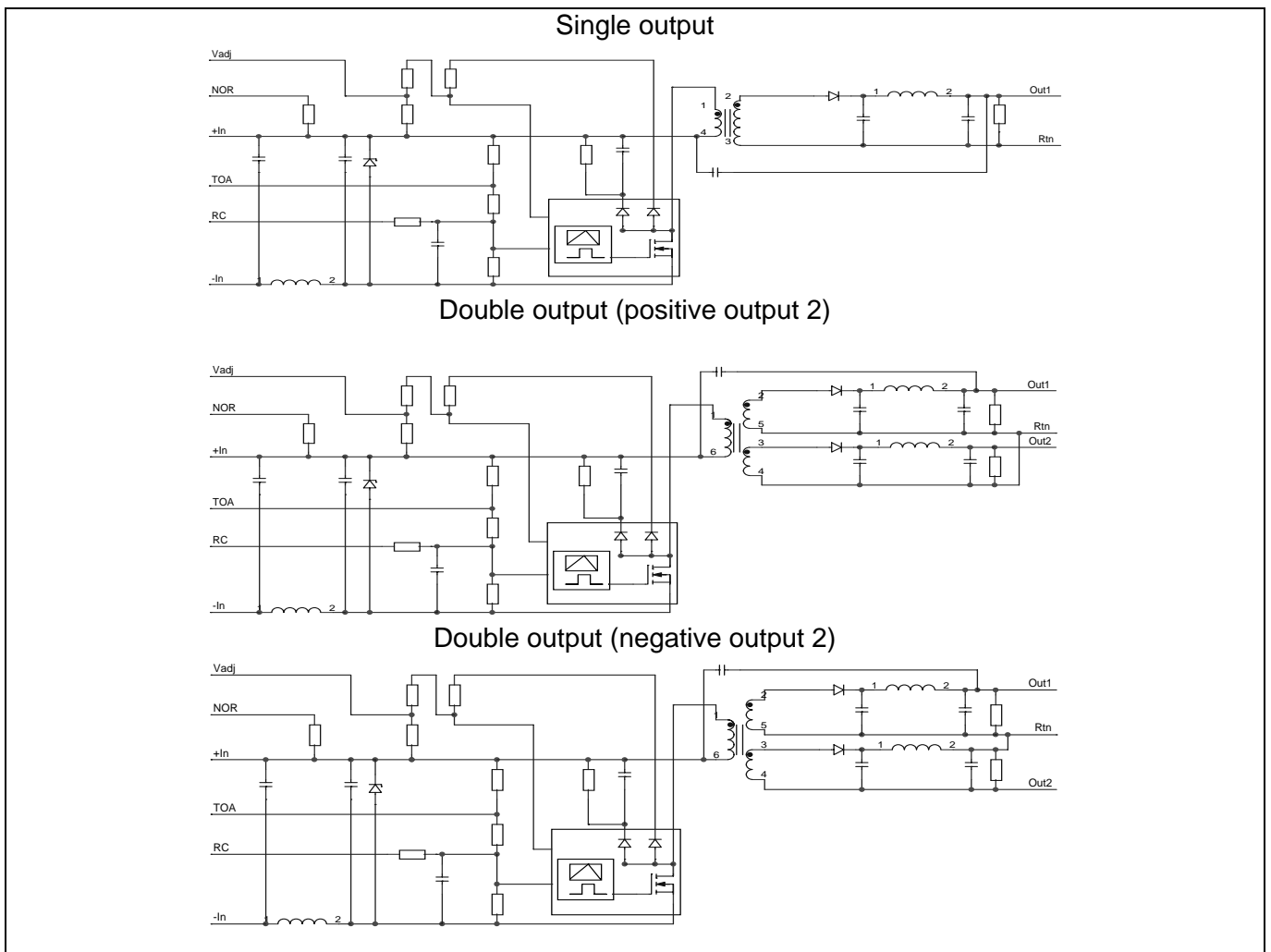
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Absolute Maximum Ratings

Characteristics		min	typ	max	Unit	
T _{ref}	Operating Temperature (see Thermal Consideration section)	-45		+110	°C	
T _s	Storage temperature	-55		+125	°C	
V _I	Input voltage	-0.5		+75	V	
V _{iso}	Isolation voltage (input to output test voltage)			1500	Vdc	
V _{Tr}	Input voltage transient (Tp 100 ms)			100	V	
V _{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option		-5	+40	V
V _{adj}	Adjust pin voltage (see Operating Information section)	-5		+40	V	

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



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2.1 V/1.5 A Electrical Specification
PKR 4310
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	34.8	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		3.2	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sinewave, 1 Vp-p		70		dB
η	Efficiency	50 % of max I_O		75		%
		max I_O		75		
		50 % of max I_O , $V_I = 48$ V		75		
		max I_O , $V_I = 48$ V		75		
P_d	Power Dissipation	max I_O		1.0	1.4	W
P_{li}	Input idling power	$I_O = 0$, $V_I = 53$ V		70		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		34		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

V_{oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, $I_O = 1.15$ A	2.07	2.12	2.17	V
	Output adjust range		1.76		2.38	V
V_O	Output voltage tolerance band	10-100% of max I_O	2.01		2.28	V
	Idling voltage	$I_O = 0$		2.5	3.0	V
	Line regulation	max I_O		14	28	mV
	Load regulation	$V_I = 53$ V, 10-100% of max I_O		130	185	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, Load step 25-75-25 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		-225 +90		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10–90 % of V_{oi})	10-100% of max I_O	0.3	0.7	0.9	ms
t_s	Start-up time (from V_I connection to 90% of V_{oi})		1.3	2.8	6	ms
I_O	Output current		0		1.5	A
I_{lim}	Current limit threshold	$V_O = 1.9$ V, $T_{ref} < \text{max } T_{ref}$	2.0	2.6	3.1	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$,		2.7	3.1	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_O .		3	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

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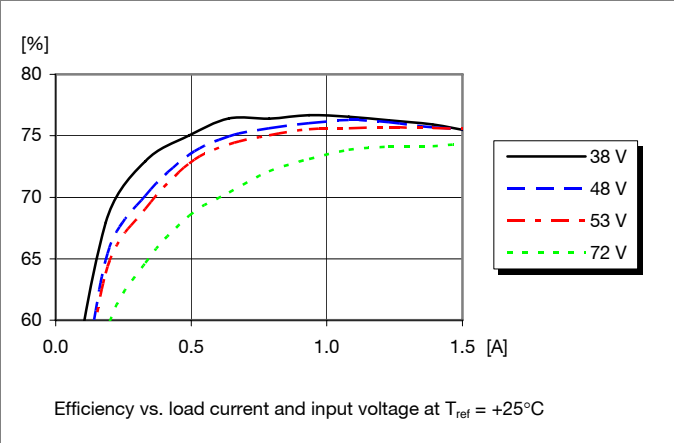
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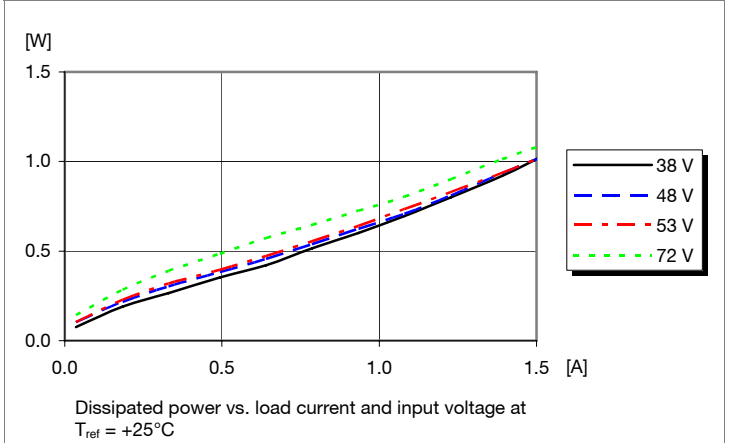
2.1 V/1.5 A Typical Characteristics

PKR 4310

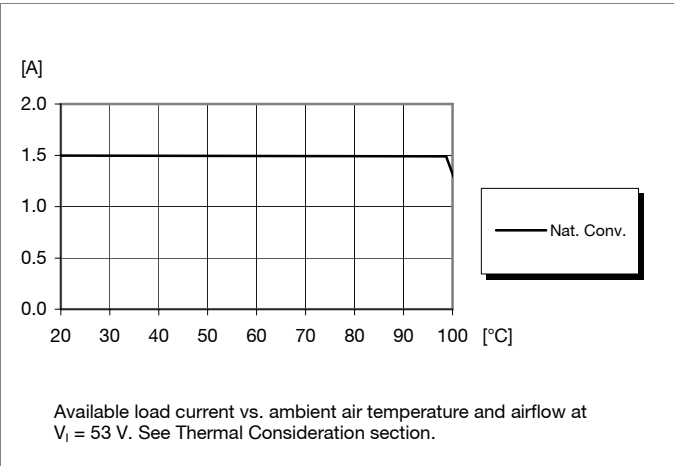
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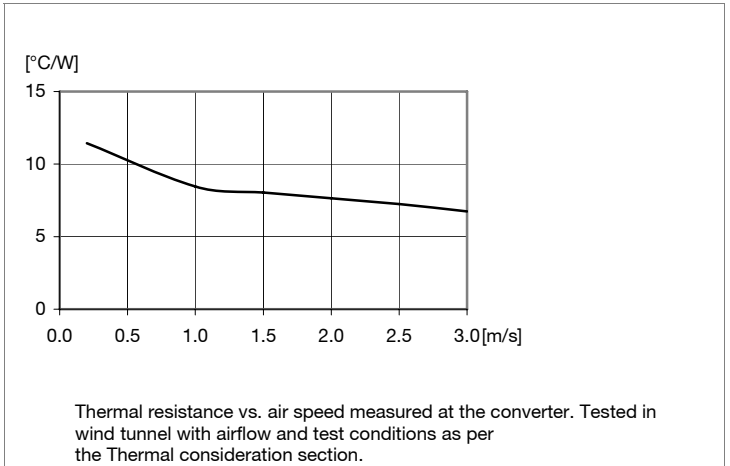
Power Dissipation



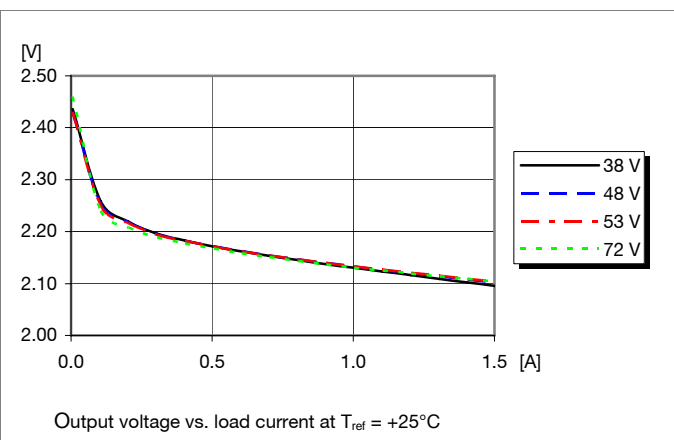
Output Current Derating



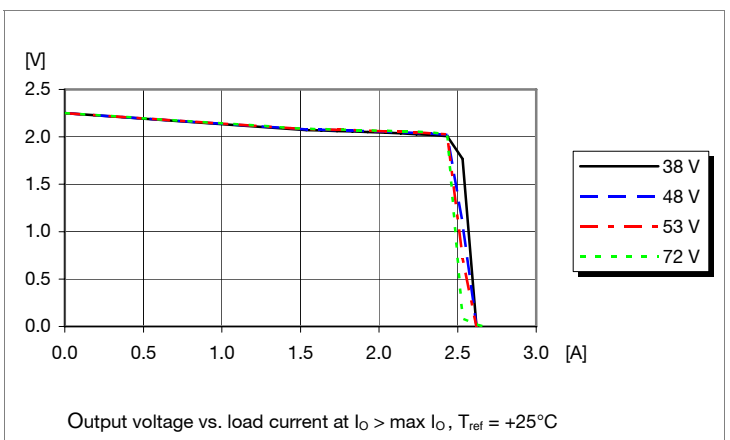
Thermal Resistance



Output Characteristics



Current Limit Characteristics



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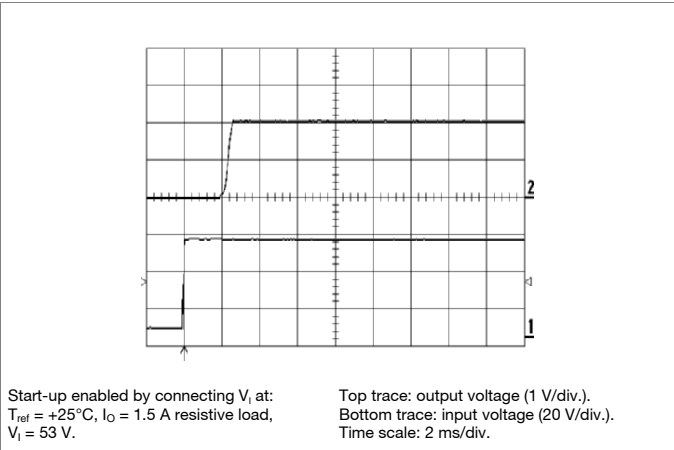
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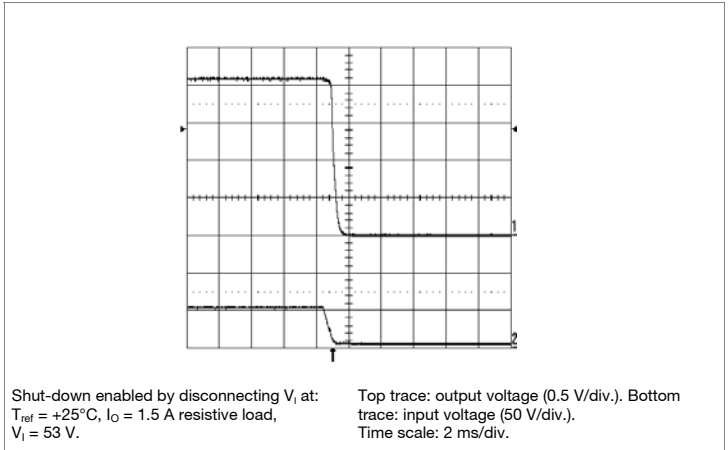
2.1 V/1.5 A Typical Characteristics

PKR 4310

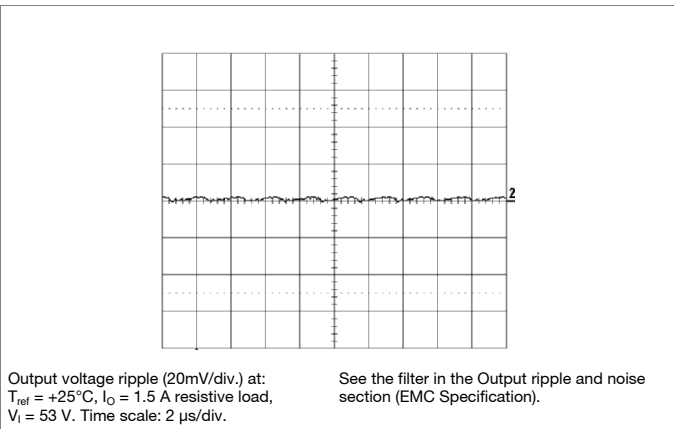
Start-up



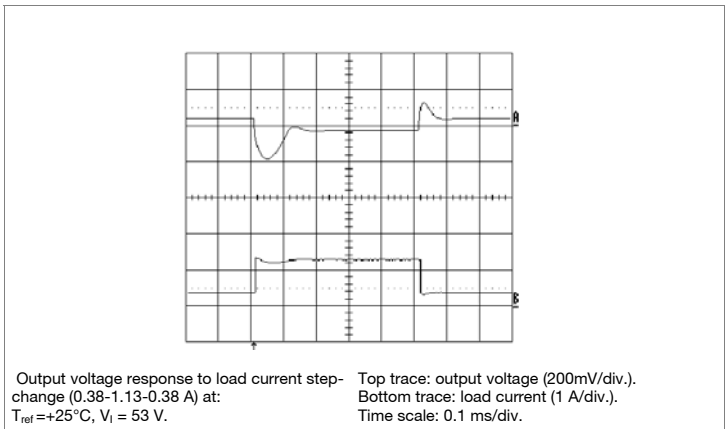
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.684 \times (2.38 - V_o) / (V_o - V_{oi}) \text{ k}\Omega$$

Eg Increase 4% => $V_o = 2.20\text{ Vdc}$

$$0.684 \times (2.38 - 2.20) / (2.20 - 2.12) = 1.54 \text{ k}\Omega$$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 2.751 \times (V_{oi} - V_o) / (V_o - 1.75) \text{ k}\Omega$$

Eg Decrease 2% => $V_o = 2.08\text{ Vdc}$

$$2.751 \times (2.12 - 2.08) / (2.08 - 1.75) = 0.33 \text{ k}\Omega$$

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3.3 V/1.5 A Electrical Specification
PKR 4510
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	34.8	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		5	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		67		dB
η	Efficiency	50 % of max I_O		80.0		%
		max I_O		80.7		
		50 % of max I_O , $V_I = 48$ V		80.4		
		max I_O , $V_I = 48$ V		80.7		
P_d	Power Dissipation	max I_O		1.2	2	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		92		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		25		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O	3.28	3.30	3.32	V
	Output adjust range		2.80		3.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	3.17		3.50	V
	Idling voltage	$I_O = 0$ A	3.40		4.0	V
	Line regulation	max I_O		3	13	mV
	Load regulation	$V_I = 53$ V, 10-100% of max I_O		90	220	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, Load step 25-75-25 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		-190 +100		mV
t_{tr}	Load transient recovery time			0.07		ms
t_r	Ramp-up time (from 10–90 % of V_O)	10-100% of max I_O	0.25	0.8	1.2	ms
t_s	Start-up time (from V_I connection to 90% of V_O)		1	3	9	ms
I_O	Output current		0		1.50	A
I_{lim}	Current limit threshold	$V_O = 2.5$ V, $T_{ref} < \max T_{ref}$	1.65	2.35	3.30	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$		2.40	3.50	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{Oi}		4	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

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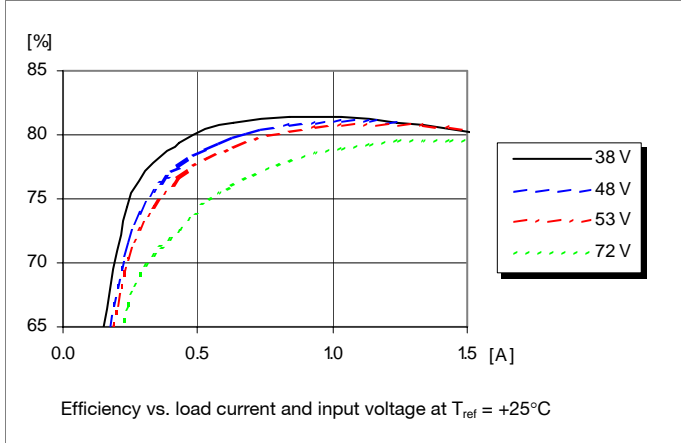
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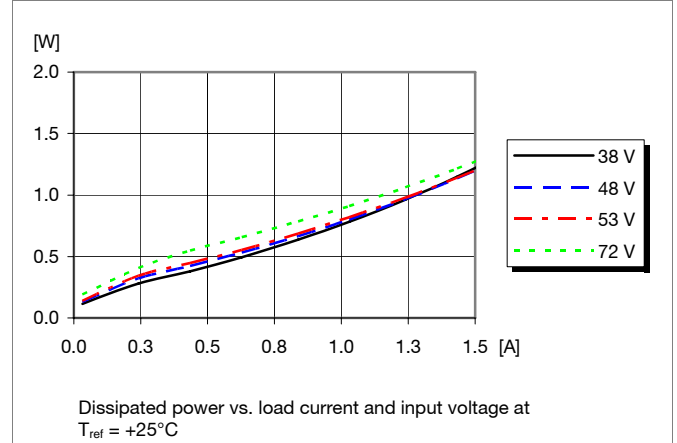
3.3 V/1.5 A Typical Characteristics

PKR 4510

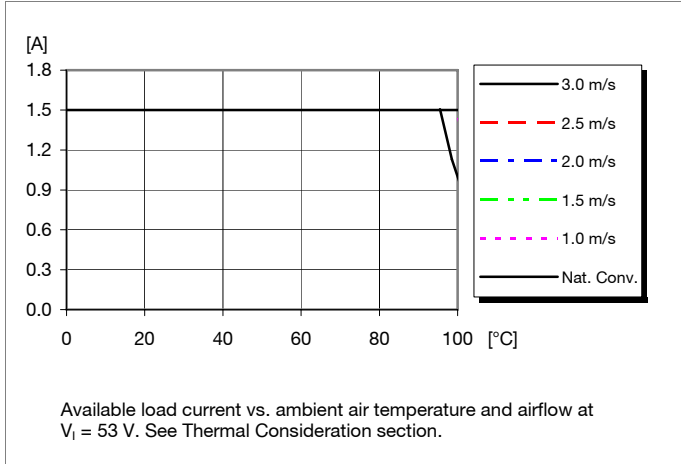
Efficiency



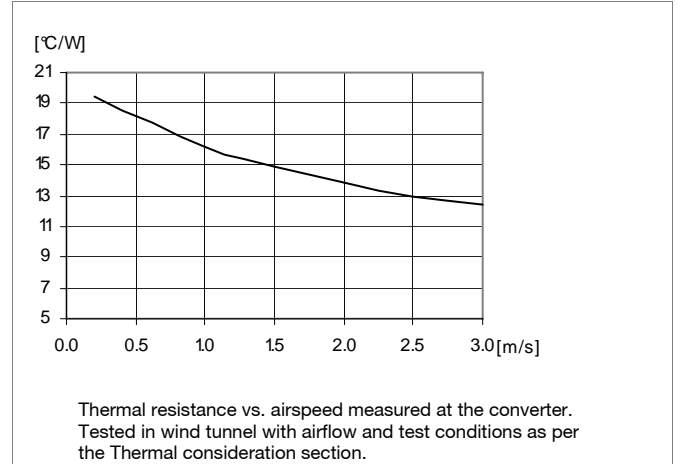
Power Dissipation



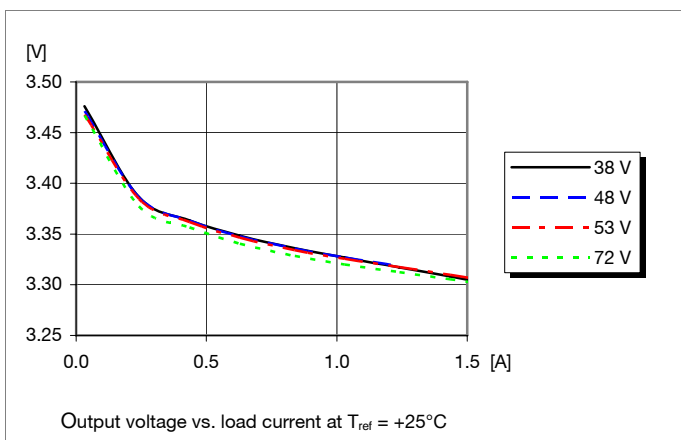
Output Current Derating



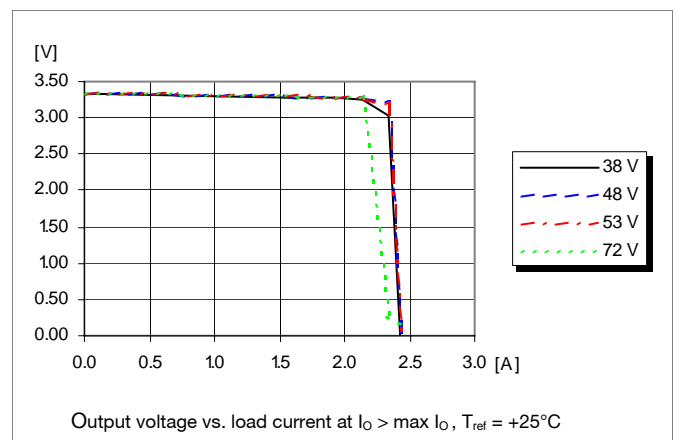
Thermal Resistance



Output Characteristics



Current Limit Characteristics



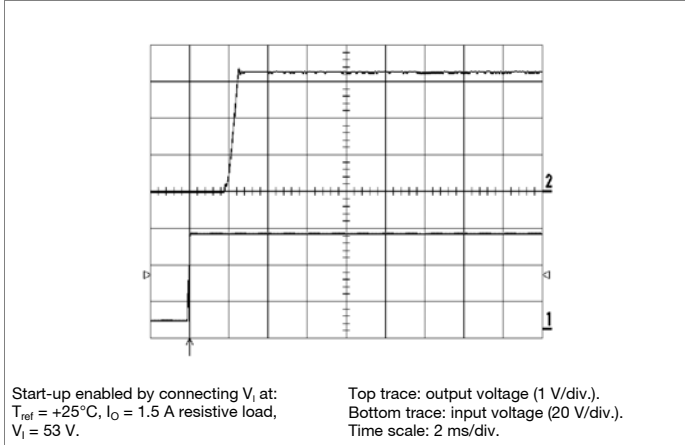
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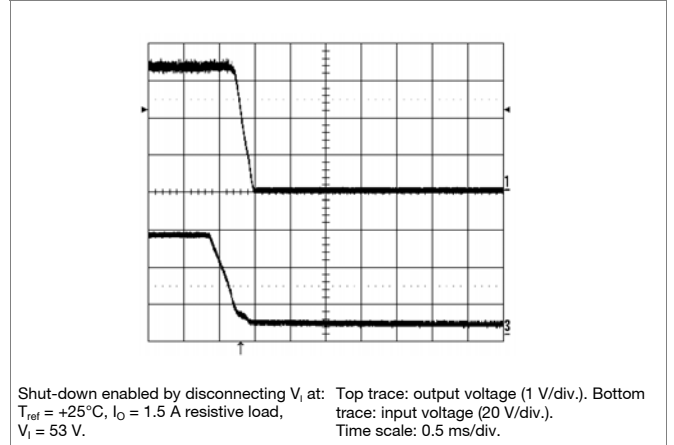
3.3 V/1.5 A Typical Characteristics

PKR 4510

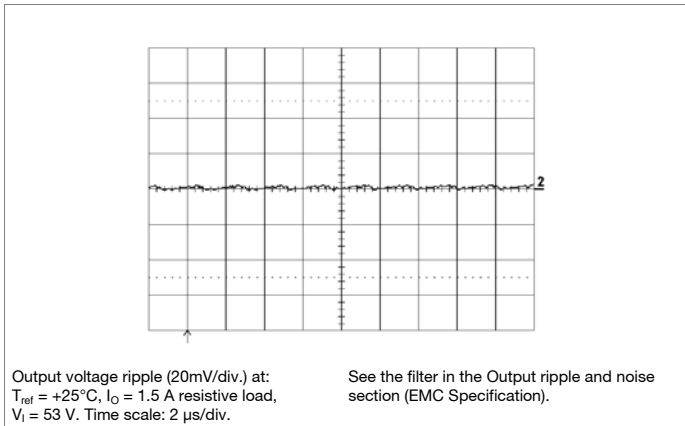
Start-up



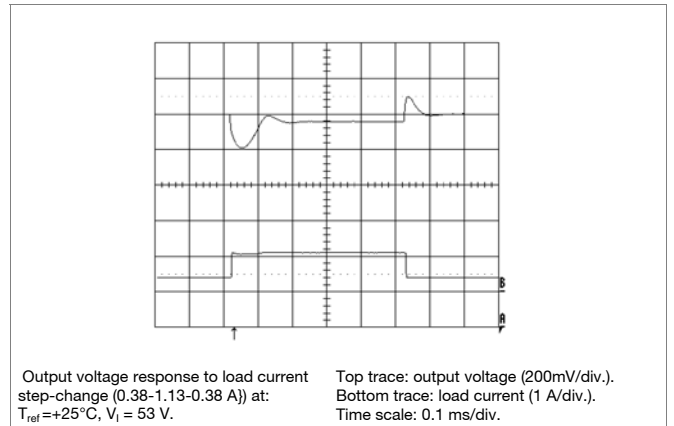
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.495 \times (3.93 - V_o) / (V_o - V_{oi}) \text{ k}\Omega$$

E.g. Increase 4% => $V_{out} = 3.43\text{ Vdc}$
 $0.495 \times (3.93 - 3.43) / (3.43 - 3.3) = 1.9\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 1.986 \times (V_{oi} - V_o) / (V_o - 2.59) \text{ k}\Omega$$

E.g. Decrease 2% => $V_{out} = 3.23\text{ Vdc}$
 $1.986 \times (3.3 - 3.23) / (3.23 - 2.59) = 0.217\text{ k}\Omega$

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5.05 V/1.2 A Electrical Specification
PKR 4611
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	35	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		68		dB
η	Efficiency	50 % of max I_O		83		%
		max I_O		84		
		50 % of max I_O , $V_I = 48$ V		83		
		max I_O , $V_I = 48$ V		84		
P_d	Power Dissipation	max I_O		1.2	1.6	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		0.13		W
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		26		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, $I_O = 0.8$ A	5.02	5.05	5.08	V
	Output adjust range		4.30		5.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.85		5.25	V
	Idling voltage	$I_O = 0$ A	5.2	5.5	6.0	V
	Line regulation	max I_O		10	20	mV
	Load regulation	$V_I = 53$ V, 10-100% of max I_O		120	210	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, Load step 25-75-25 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		-225 +140		mV
t_{tr}	Load transient recovery time			0.15		ms
t_r	Ramp-up time (from 10–90 % of V_O)	10-100% of max I_O	0.5	1.0	2.0	ms
t_s	Start-up time (from V_I connection to 90% of V_O)		1.7	3.0	7.0	ms
I_O	Output current		0		1.2	A
I_{lim}	Current limit threshold	$V_O = 4.0$ V, $T_{ref} < \max T_{ref}$	1.4	1.7	2.1	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$		1.8	2.6	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{Oi}		5	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

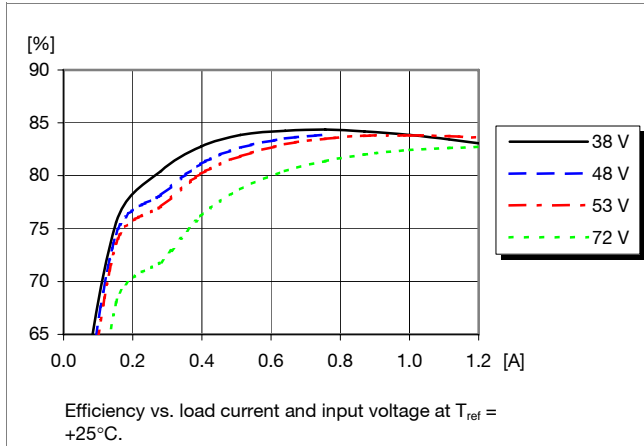
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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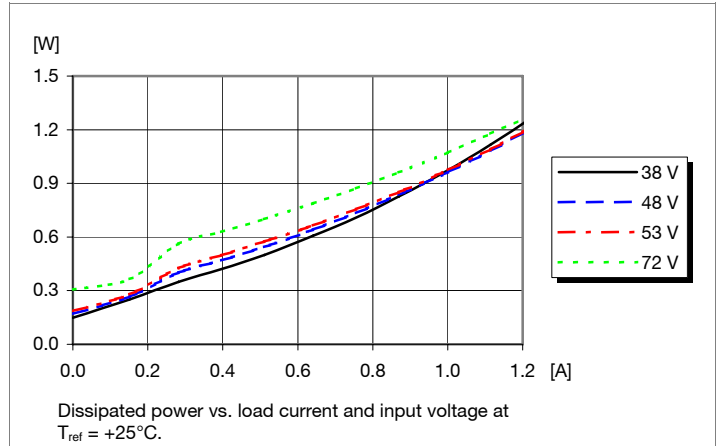
5.05 V/1.2 A Typical Characteristics

PKR 4611

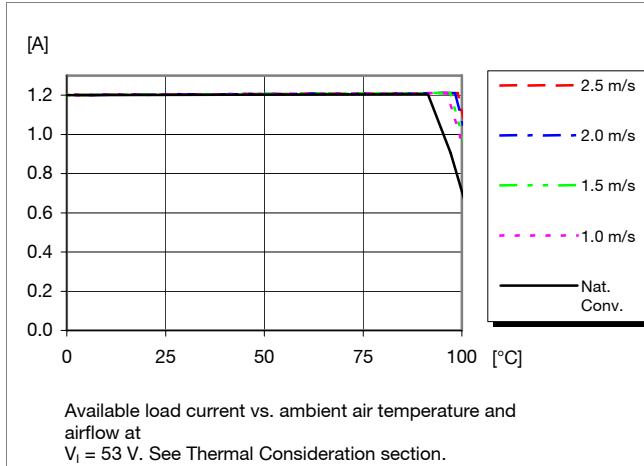
Efficiency



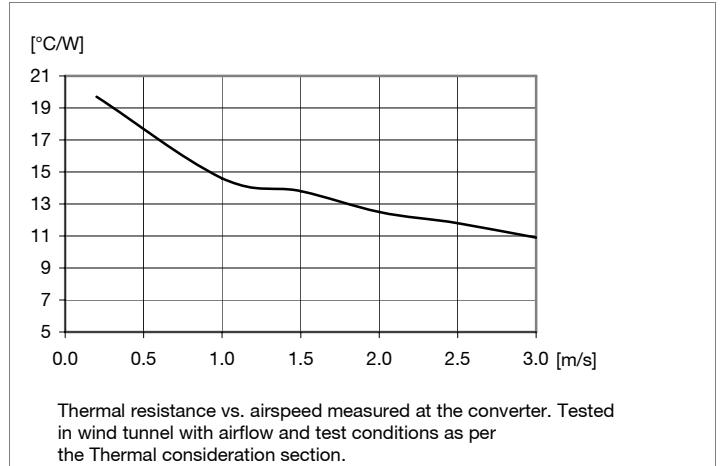
Power Dissipation



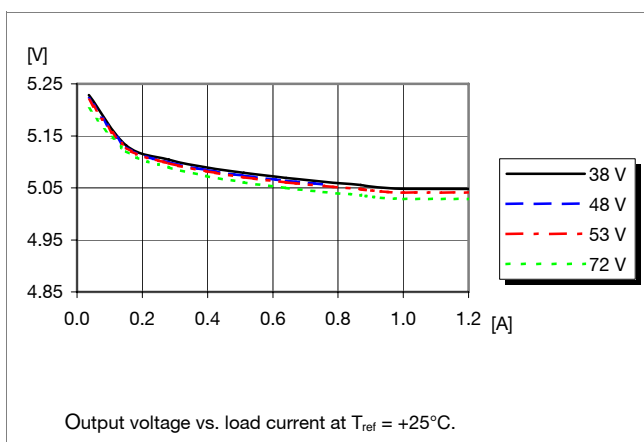
Output Current Derating



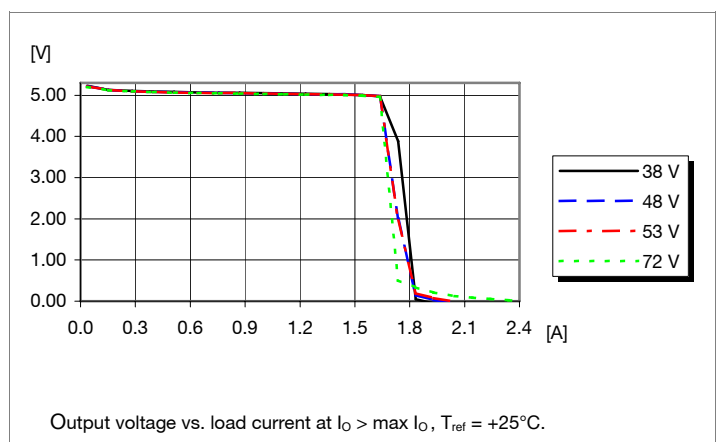
Thermal Resistance



Output Characteristics



Current Limit Characteristics



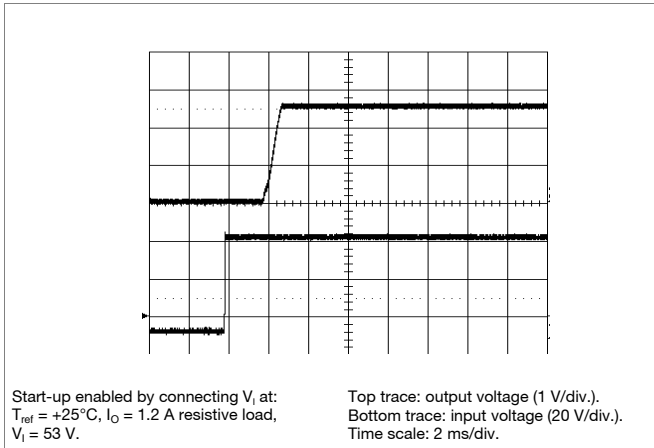
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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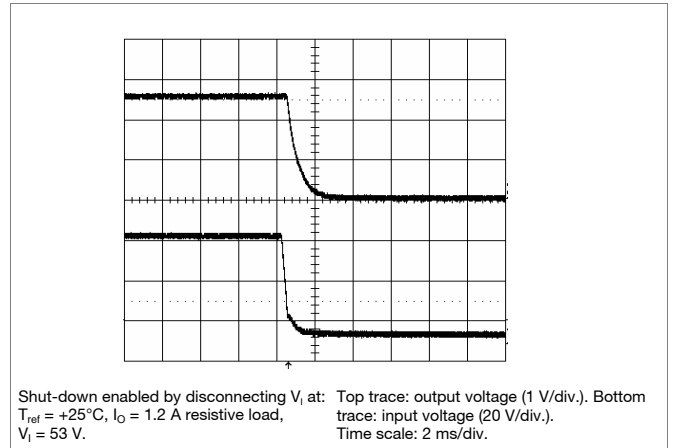
5.05 V/1.2 A Typical Characteristics

PKR 4611

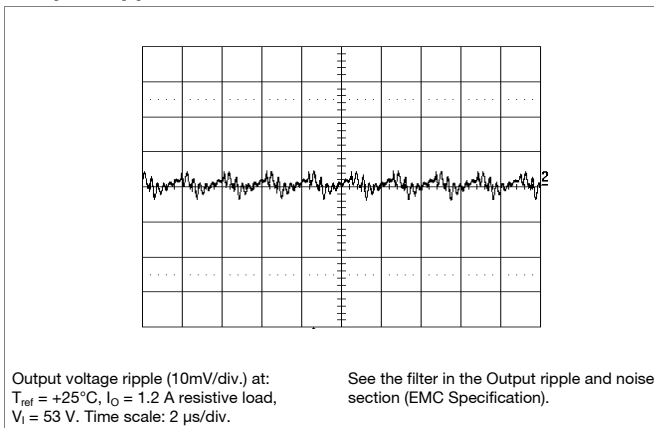
Start-up



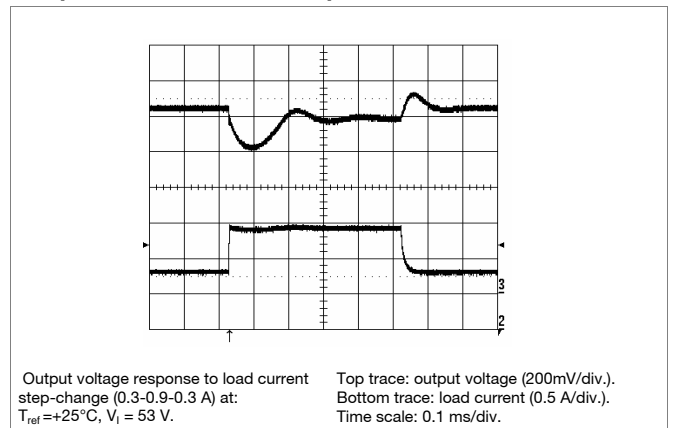
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.495 \times (5.87 - V_O) / (V_O - 5.05) \text{ k}\Omega$$

E.g. Increase 4% => $V_{out} = 5.25\text{ Vdc}$
 $0.495 \times (5.87 - 5.25) / (5.25 - 5.05) = 1.5 \text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 1.986 \times (5.05 - V_O) / (V_O - 4.12) \text{ k}\Omega$$

E.g. Decrease 2% => $V_{out} = 4.95\text{ Vdc}$
 $1.986 \times (5.05 - 4.95) / (4.95 - 4.12) = 0.239 \text{ k}\Omega$

PKR 4000 series DC/DC converters, Input 36-75 V, Output 1.5 A/7 W	EN/LZT 146 302 R6D Apr 2007
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12 V/0.6 A Electrical Specification
PKR 4713
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	35.1	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		7	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		60		dB
η	Efficiency	50 % of max I_O		86		%
		max I_O		85		
		50 % of max I_O , $V_I = 48$ V		86		
		max I_O , $V_I = 48$ V		85		
P_d	Power Dissipation	max I_O		1.25	1.75	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		87		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		33		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

V_{Oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, 50% of max I_O	11.83	12.0	12.18	V
	Output adjust range		10.2		13.8 (15)	V
V_O	Output voltage tolerance band	10-100% of max I_O	11.5		12.5	V
	Idling voltage	$I_O = 0$ A	13.3		18.6	V
	Line regulation	max I_O		60	140	mV
	Load regulation	$V_I = 53$ V, 10-100% of max I_O		340	650	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, Load step 25-75-25 % of max I_O , $di/dt = 5$ A/ μs , see Note 1		+100 -300		mV
t_{tr}	Load transient recovery time			0.30		ms
t_r	Ramp-up time (from 10-90 % of V_{Oj})		0.6	0.8	1.0	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oj})	10-100% of max I_O	1	3	12	ms
I_O	Output current		0		0.6	A
I_{lim}	Current limit threshold	$V_O = 10$ V, $T_{ref} < \max T_{ref}$	0.65	0.90	1.2	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$		1.2	1.9	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{Oj}		10	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

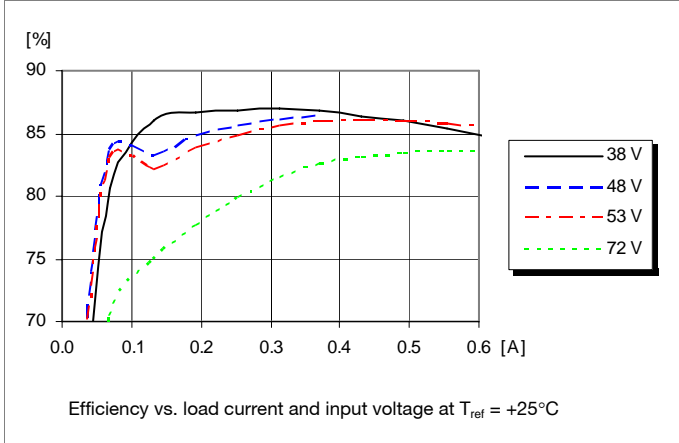
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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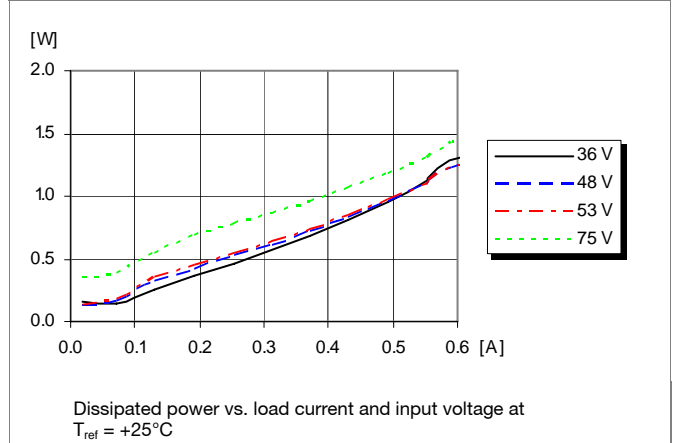
12 V/0.6 A Typical Characteristics

PKR 4713

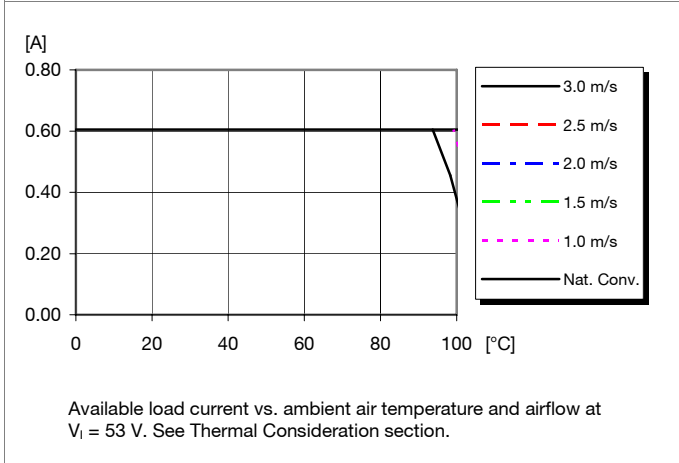
Efficiency



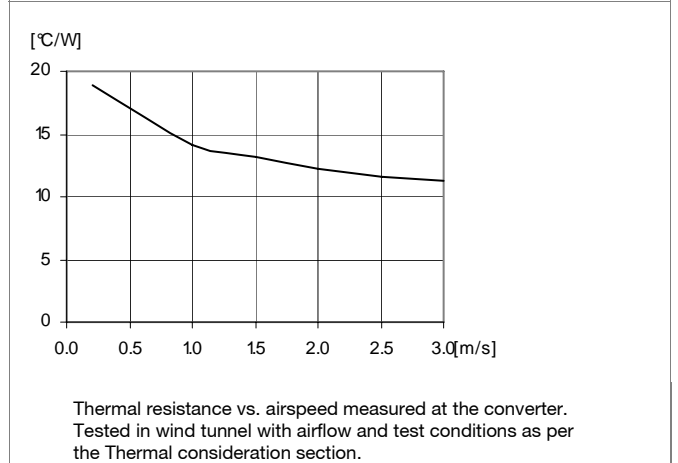
Power Dissipation



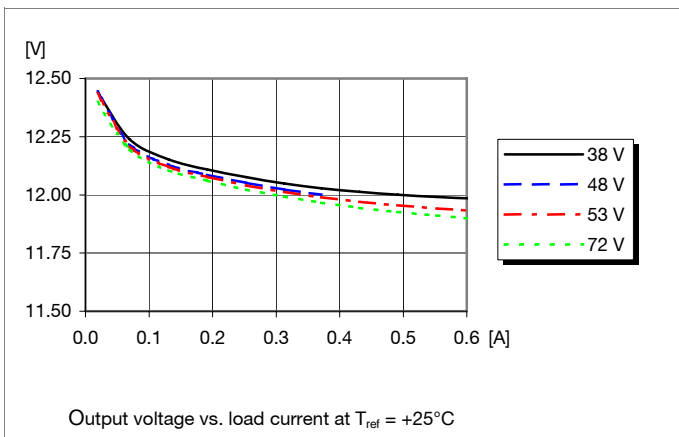
Output Current Derating



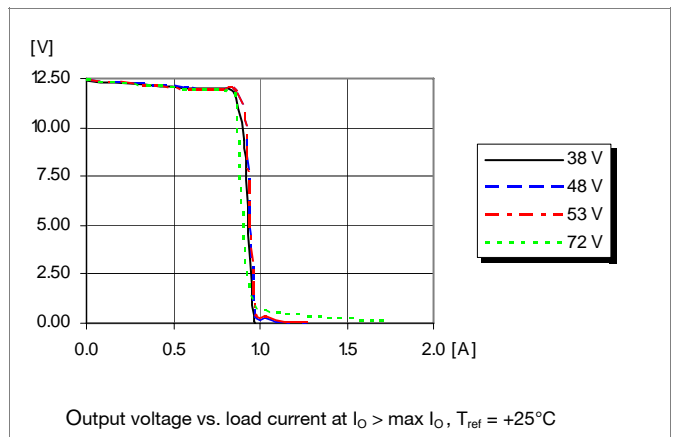
Thermal Resistance



Output Characteristics



Current Limit Characteristics



PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

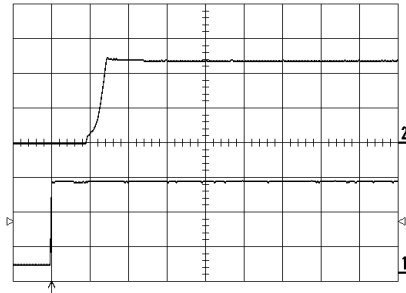
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12 V/0.6 A Typical Characteristics

PKR 4713

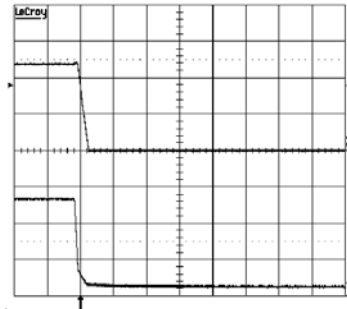
Start-up



Start-up enabled by connecting V_i at:
 $T_{ref} = +25^{\circ}\text{C}$, $I_o = 0.6\text{A}$ resistive load,
 $V_i = 53\text{V}$

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (20 V/div.).
Time scale: 2 ms/div.

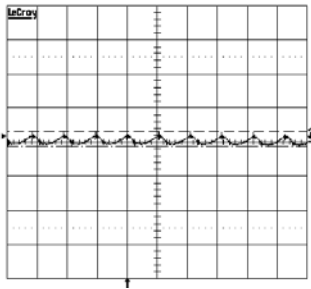
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{ref} = +25^{\circ}\text{C}$, $I_o = 0.6\text{A}$ resistive load,
 $V_i = 53\text{V}$

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (20 V/div.).
Time scale: 2 ms/div.

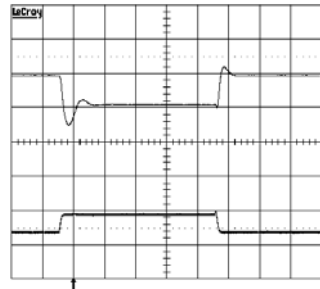
Output Ripple & Noise



Output voltage ripple (10mV/div.) at:
 $T_{ref} = +25^{\circ}\text{C}$, $I_o = 0.6\text{A}$ resistive load,
 $V_i = 53\text{V}$ Time scale: 2 μs /div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step- change (0.15-0.45-0.15 A) at:
 $T_{ref} = +25^{\circ}\text{C}$, $V_i = 53\text{V}$

Top trace: output voltage (200mV/div.).
Bottom trace: load current (1 A/div.).
Time scale: 0.2 ms/div.

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

To adjust the output voltage upwards, a resistor is connected between pins 8 and 18. Pins 8 and 9 have to be shorted. The output voltage increases when the resistance decreases. The resistance value is given by the equation:

$R_{ou} = 0.566 \cdot (15 - V_o) / (V_o - 12)$, (kOhm); V_o is the desired output voltage. Over 13.8V output voltage the input voltage range is limited to 38...65 V.

To adjust the output voltage downwards, a resistor is connected between pins 8 and 9. The output voltage decreases when the connected resistance value increases. The resistance value is given by the equation:

$R_{od} = 2.284 \cdot (12 - V_o) / (V_o - 9.52)$, (kOhm), V_o is the desired output voltage.

PKR 4000 series DC/DC converters, Input 36-75 V, Output 1.5 A/7 W	EN/LZT 146 302 R6D Apr 2007
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Dual ±12 V/0.25 A, Electrical Specification
PKR 4621
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	35.7	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		66		dB
η	Efficiency	$I_{O1} = 0.12$ A, $I_{O2} = 0.12$ A		86		%
		$I_{O1} = 0.12$ A, $I_{O2} = 0.12$ A		86		
		$I_{O1} = 0.12$ A, $I_{O2} = 0.12$ A, $V_I = 48$ V		86		
		$I_{O1} = 0.25$ A, $I_{O2} = 0.25$ A, $V_I = 48$ V		86		
P_d	Power Dissipation	$I_{O1} = I_{O2} = 0.25$ A		1.0	1.3	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		100		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		32		mW
f_s	Switching frequency	$I_{O1} = I_{O2} = 0.12 \dots 0.25$ A	412	485	558	kHz

			Output 1			Output 2			Unit
			min	typ	max	min	typ	max	
V_{Oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, $I_{O1} = I_{O2} = 0.15$ A, see Note 2, 3	11.83	12.0	12.18		12.0		V
	Output adjust range		10.2		13.8	10.2		13.8	V
V_O	Output voltage tolerance band	10-100% of max I_O	11.5		12.5	11.4		12.6	V
	Idling voltage	$I_O = 0$	13		20	13		20	V
	Line regulation	$I_{O1} = I_{O2} = 0.25$ A		23	90		22	80	mV
	Load regulation output 1	$V_I = 53$ V, $I_{O1} = 0.025 \dots 0.25$ A, $I_{O2} = 0.25$ A		340	470				mV
	Load regulation output 2	$V_I = 53$ V, $I_{O2} = 0.025 \dots 0.25$ A, $I_{O1} = 0.25$ A					340	455	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, load step $I_{O1} = 0.1-0.2-0.1$ A, $I_{O2} = 0.25$ A, $di/dt = 1$ A/ μs , see Note 1		-210 +56			-200 +56		mV
t_{tr}	Load transient recovery time			0.2		0.2		ms	
t_r	Ramp-up time (from 10-90% of V_{Oi})	$I_{O1} = I_{O2} = 0.025 \dots 0.25$ A	0.4	1.0	2.9	0.4	1.0	2.9	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.5	3.2	7	1.5	3.2	7	ms
I_O	Output current		0		0.5	0		0.5	A
I_{lim}	Current limit threshold	$V_O = 10$ V, $T_{ref} < \text{max } T_{ref}$	0.35	0.76	1.1	0.35	0.76	1.1	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$		1.1	1.7		1.1	1.7	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{Oi}		6	50		6	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

Note 2: Output voltage on Output 2 is negative (-12V)

Note 3: Can be adjusted to 14.4V. Over 13.8V output voltage, the input voltage range is limited to 38...65V

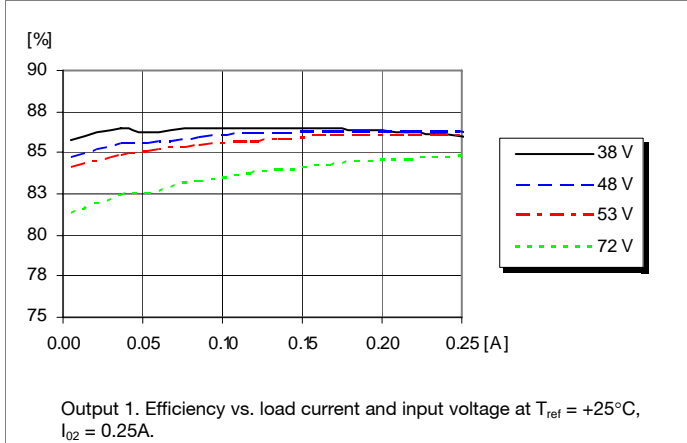
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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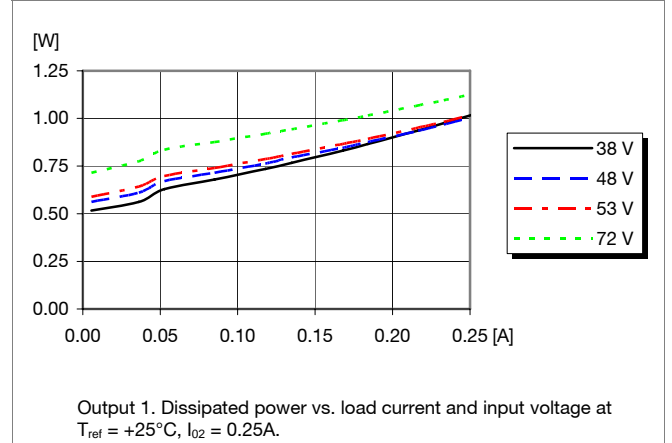
Dual ±12 V/0.25 A, Typical Characteristics

PKR 4621

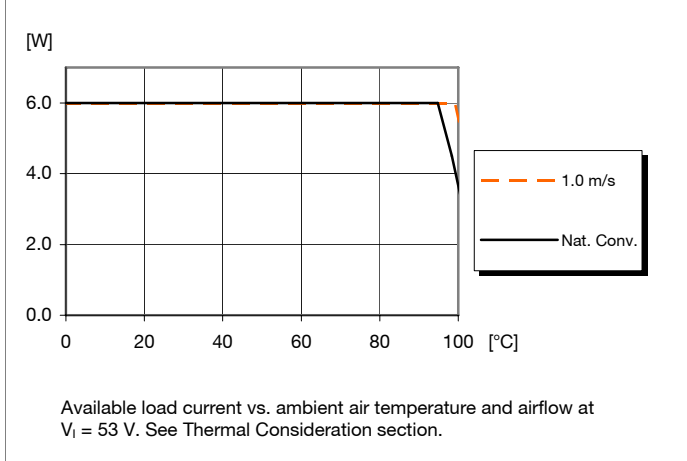
Efficiency



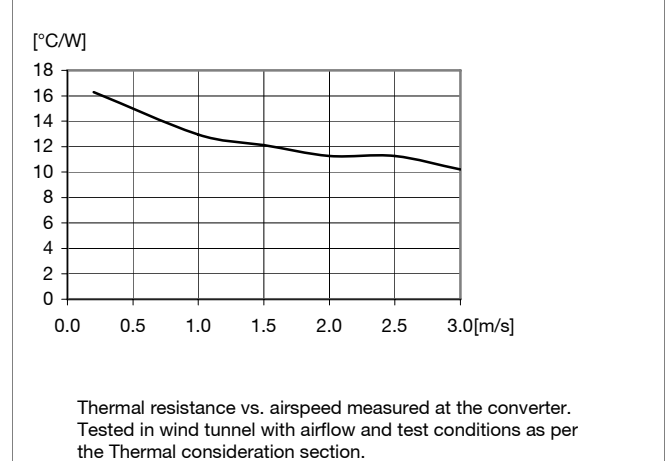
Power Dissipation



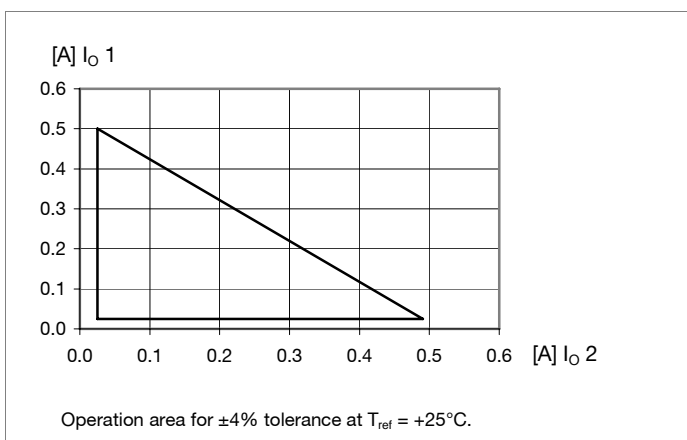
Output Power Derating



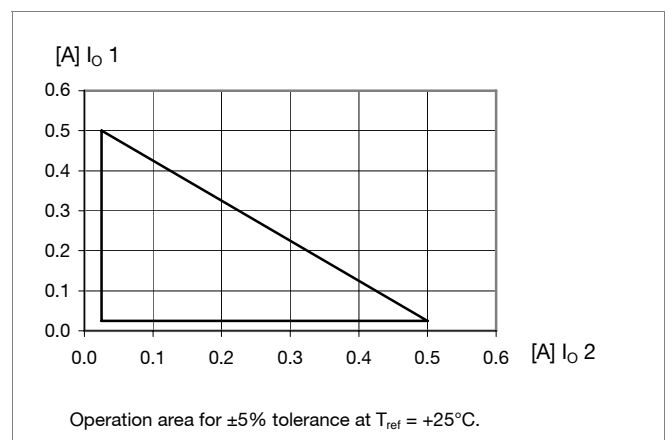
Thermal Resistance



Cross regulation Output 1 (+12V)



Cross regulation Output 2 (-12V)



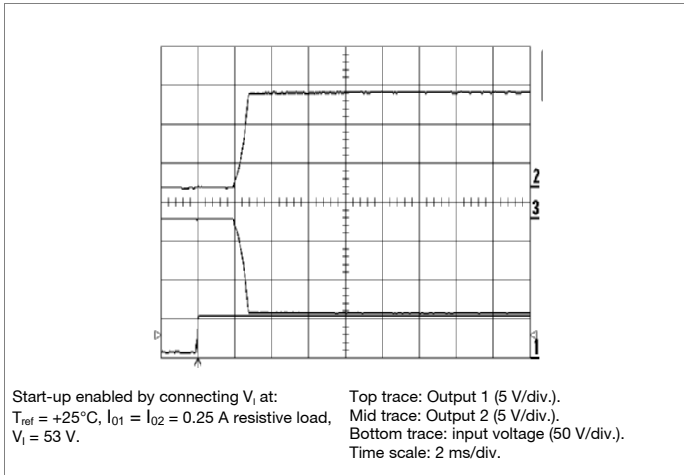
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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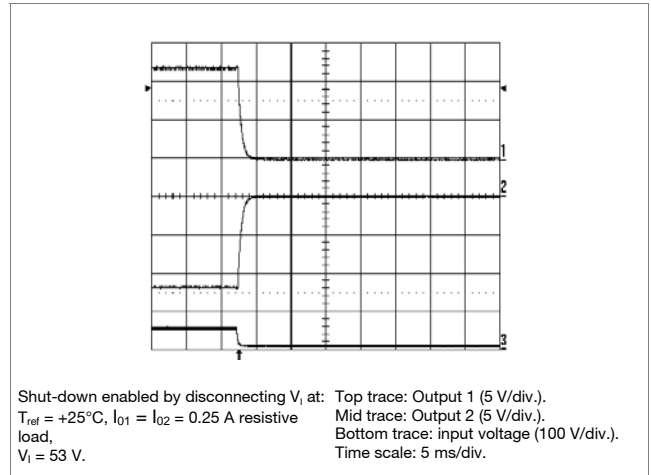
Dual ±12 V/0.25 A, Typical Characteristics

PKR 4621

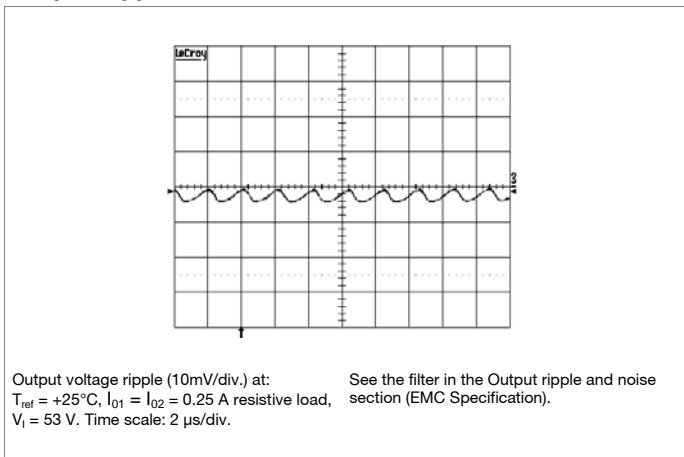
Start-up



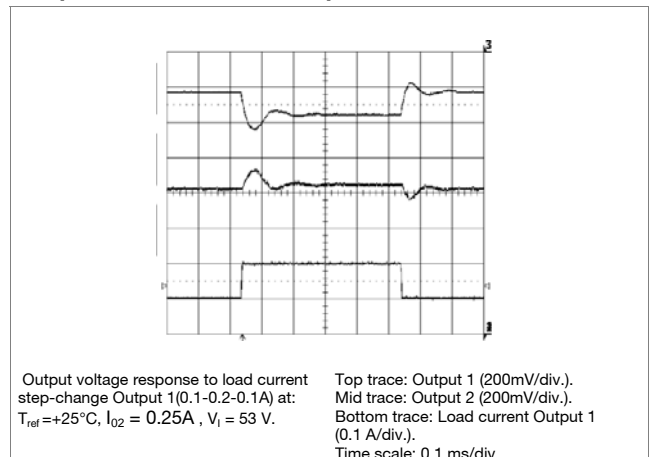
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.566 \times (14.4 - V_o) / (V_o - 12) \text{ k}\Omega$$

E.g. Increase 4% => $V_{out} = 12.48\text{ Vdc}$

$$0.566 \times (14.4 - 12.48) / (12.48 - 12) = 2.26 \text{ k}\Omega$$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 2.284 \times (12 - V_o) / (V_o - 9.52) \text{ k}\Omega$$

E.g. Decrease 2% => $V_{out} = 11.76\text{ Vdc}$

$$2.284 \times (12 - 11.76) / (11.76 - 9.52) = 0.245 \text{ k}\Omega$$

PKR 4000 series DC/DC converters, Input 36-75 V, Output 1.5 A/7 W	EN/LZT 146 302 R6D Apr 2007
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Dual ±5 V/0.6 A, Electrical Specification
PKR 4622
 $T_{ref} = -30$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	32.0	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	35.3	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		64		dB
η	Efficiency	$I_{O1} = 0.3$ A, $I_{O2} = 0.3$ A		84		%
		$I_{O1} = 0.6$ A, $I_{O2} = 0.6$ A		84		
		$I_{O1} = 0.3$ A, $I_{O2} = 0.3$ A, $V_I = 48$ V		84		
		$I_{O1} = 0.6$ A, $I_{O2} = 0.6$ A, $V_I = 48$ V		84		
P_d	Power Dissipation	$I_{O1} = 0.6$ A, $I_{O2} = 0.6$ A		1.2	1.5	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		150		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		30		mW
f_s	Switching frequency	$I_{O1} = I_{O2} = 0.3 \dots 0.6$ A	412	485	558	kHz

			Output 1			Output 2			Unit
			min	typ	max	min	typ	max	
V_{Oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, $I_{O1} = 0.3$ A, $I_{O2} = 0.3$ A, see Note 2	5.02	5.05	5.08		5.05		V
	Output adjust range		4.30		5.80	4.30		5.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.85		5.25	4.80		5.30	V
	Idling voltage	$I_O = 0$ A	5.1		6.3	5.1		6.3	V
	Line regulation	$I_{O1} = 0.6$ A, $I_{O2} = 0.6$ A		18	52		23	59	mV
	Load regulation output 1	$V_I = 53$ V, $I_{O1} = 0.06 \dots 0.6$ A, $I_{O2} = 0.6$ A		190	300				mV
	Load regulation output 2	$V_I = 53$ V, $I_{O2} = 0.06 \dots 0.6$ A, $I_{O1} = 0.6$ A					180	280	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, load step $I_{O1} = 0.3-0.45-0.3$ A, $I_{O2} = 0.6$ A, $di/dt = 1$ A/ μs , see Note 1		± 80			± 80		mV
t_{tr}	Load transient recovery time			0.15		0.15		ms	
t_r	Ramp-up time (from 10-90% of V_{Oi})	$I_{O1} = I_{O2} = 0.025 \dots 0.25$ A	0.1	0.7	1.6	0.1	0.7	1.6	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.5	2.8	7	1.5	2.8	7	ms
I_O	Output current		0		1.0	0		1.0	A
I_{lim}	Current limit threshold	$V_O = 4.0$ V, $T_{ref} < \max T_{ref}$	1.1	1.7	2.4	1.1	1.7	2.4	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$		1.9	2.9		1.9	2.9	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{Oi}		11	50		13	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

Note 2: Output voltage on Output 2 is negative (-5V)

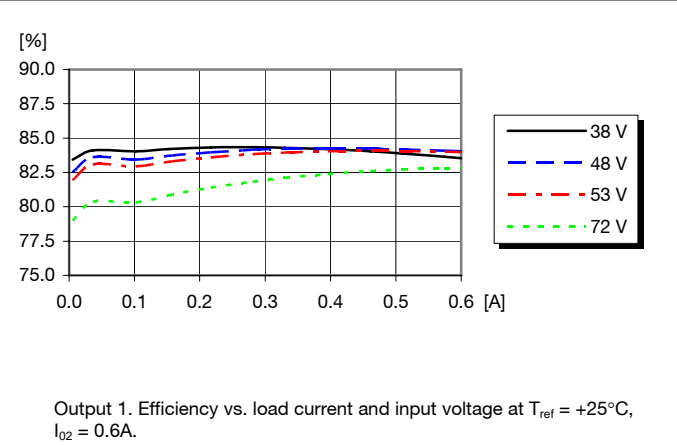
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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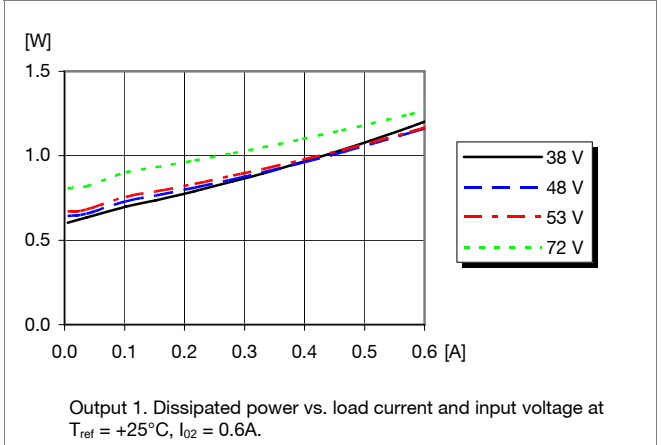
Dual ±5 V/0.6 A, Typical Characteristics

PKR 4622

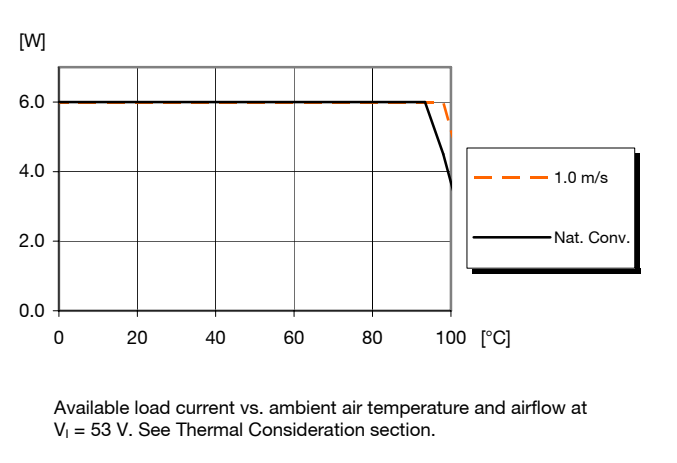
Efficiency



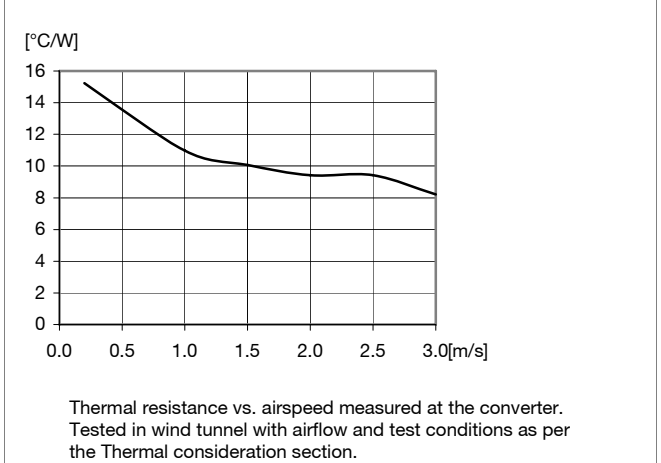
Power Dissipation



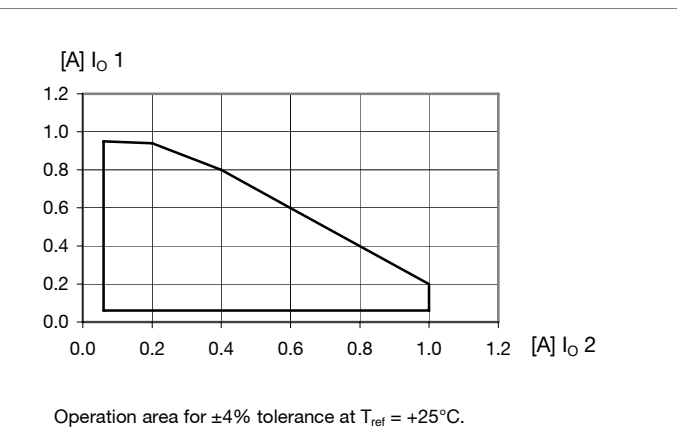
Output Power Derating



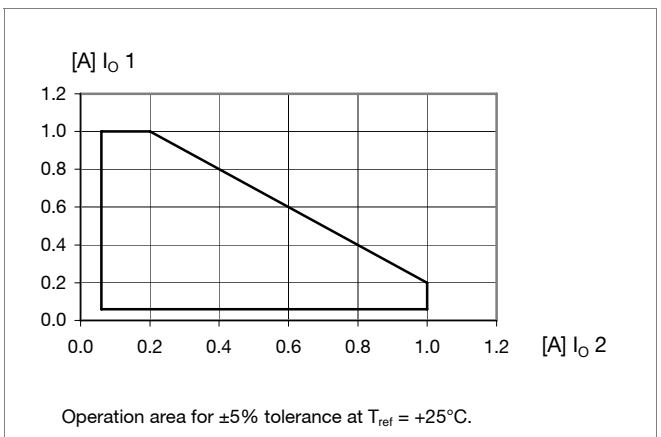
Thermal Resistance



Cross regulation Output 1 (+5V)



Cross regulation Output 2 (-5V)



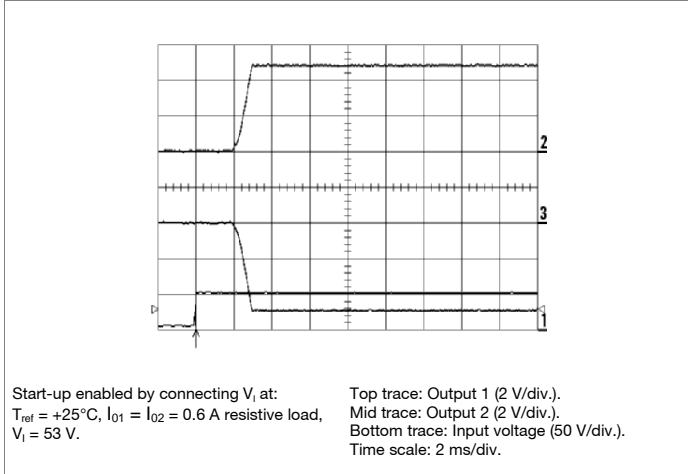
PKR 4000 series
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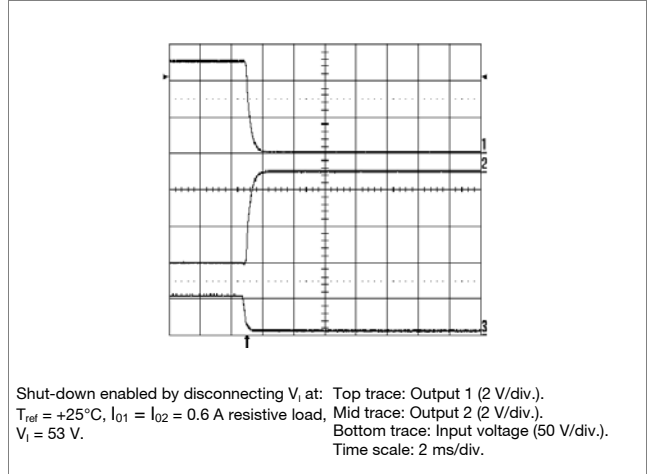
Dual ±5 V/0.6 A, Typical Characteristics

PKR 4622

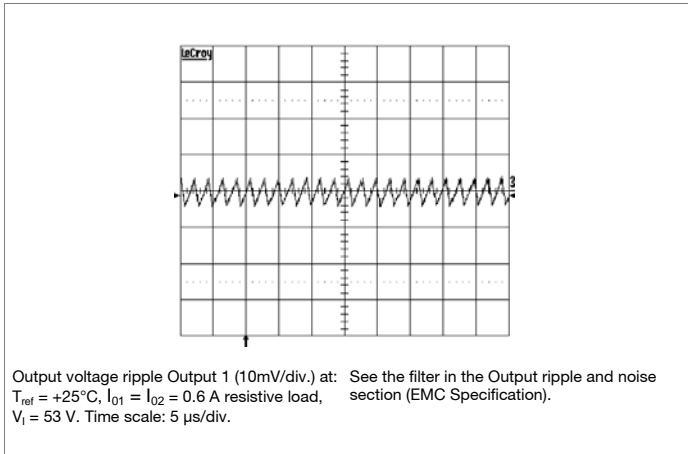
Start-up



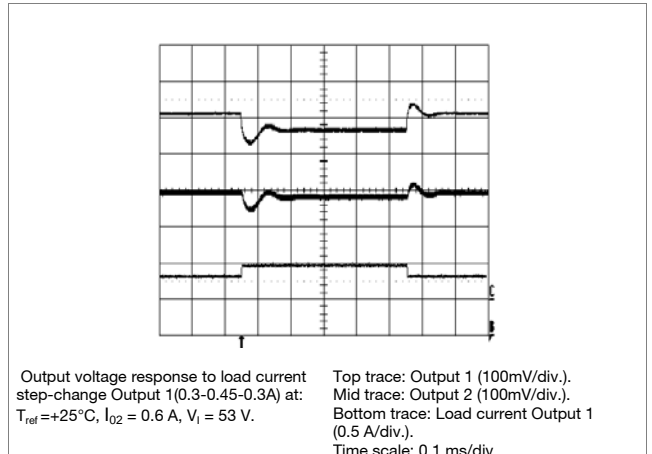
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.495 \times (5.87 - V_o) / (V_o - 5.05) \text{ k}\Omega$$

E.g. Increase 4% => $V_{out} = 5.25\text{ Vdc}$
 $0.495 \times (5.87 - 5.25) / (5.25 - 5.05) = 1.53 \text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 1.986 \times (5.05 - V_o) / (V_o - 4.12) \text{ k}\Omega$$

E.g. Decrease 2% => $V_{out} = 4.95\text{ Vdc}$
 $1.986 \times (5.05 - 4.95) / (4.95 - 4.12) = 0.239 \text{ k}\Omega$

PKR 4000 series DC/DC converters, Input 36-75 V, Output 1.5 A/7 W	EN/LZT 146 302 R6D Apr 2007
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Dual 5 V/0.6 A, 3.3 V/0.9 A, Electrical Specification
PKR 4628
 $T_{ref} = 38$ to $+85^{\circ}\text{C}$, $V_I = 38$ to 72 V, unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, max I_O unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{loff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{lon}	Turn-on input voltage	Increasing input voltage	32	35	38	V
C_I	Internal input capacitance			2		μF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sine wave, 1 Vp-p		60		dB
η	Efficiency	$I_{O1} = 0.3$ A, $I_{O2} = 0.45$ A		82		%
		$I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A		83		
		$I_{O1} = 0.3$ A, $I_{O2} = 0.45$ A, $V_I = 48$ V		82		
		$I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A, $V_I = 48$ V		83		
P_d	Power Dissipation	$I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A		1.3	1.4	W
P_{li}	Input idling power	$I_O = 0$ A, $V_I = 53$ V		105		mW
P_{RC}	Input standby power	$V_I = 53$ V (turned off with RC)		30		mW
f_s	Switching frequency	$I_{O1} = 0.3\dots 0.6$ A, $I_{O2} = 0.45\dots 0.9$ A	412	485	558	kHz

		Conditions	Output 1			Output 2			Unit
			min	typ	max	min	typ	max	
V_{O1}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 53$ V, $I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A		5.20		3.25	3.27	3.29	V
	Output adjust range		4.43		5.97		2.80	3.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.94		5.46		3.17	3.42	V
	Idling voltage	$I_O = 0$ A	5.5		6.6		3.5	4.29	V
	Line regulation	$I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A		28	100		10	25	mV
	Load regulation output 1	$V_I = 53$ V, $I_{O1} = 0.06\dots 0.6$ A, $I_{O2} = 0.9$ A		148	290				mV
	Load regulation output 2	$V_I = 53$ V, $I_{O2} = 0.09\dots 0.9$ A, $I_{O1} = 0.6$ A					145	240	mV
V_{tr}	Load transient voltage deviation	$V_I = 53$ V, Load step of $I_{O1} = 0.6$ A 25-75-25%, $I_{O2} = 0.9$ A		-300 +120			-120 +80		mV
t_{tr}	Load transient recovery time	$di/dt = 1$ A/ μs , see Note 1		150			150		μs
t_r	Ramp-up time (from 10-90% of V_{O1})	$I_{O1} = 0.6$ A, $I_{O2} = 0.9$ A	0.2	1.1	7	0.1	1.0	6.2	ms
t_s	Start-up time (from V_I connection to 90% of V_{O1})		1.6	3.4	8	1.6	3.4	8	ms
I_O	Output current		0	0.6	1	0	0.9	1	A
I_{lim}	Current limit threshold	$V_{O1} = 4.0$ V, $V_{O2} = 2.5$ V $T_{ref} < \max T_{ref}$	1.5	1.6	2.2	1.5	1.6	2.5	A
I_{sc}	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$,		2.0	2.8		2.3	3.1	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{O1}		20	80		6	50	mVp-p

Note 1: Output filter according to Ripple & Noise section.

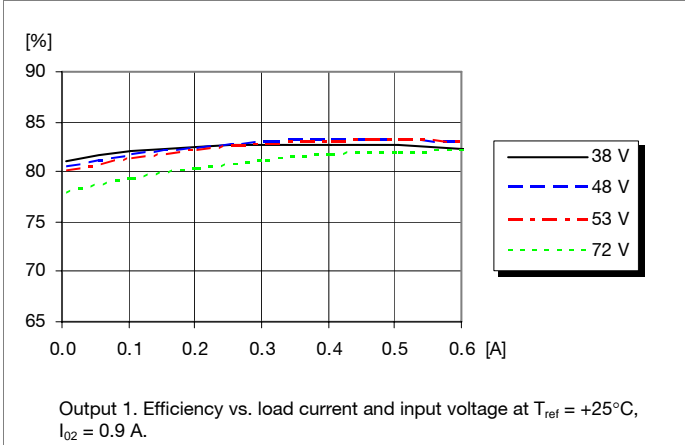
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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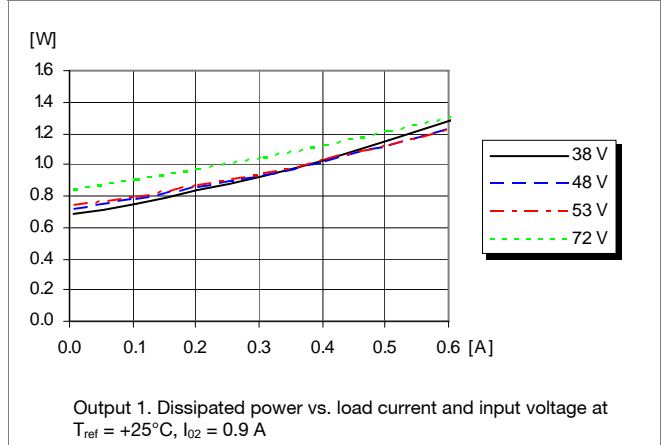
Dual 5 V/0.6 A, 3.3 V/0.9 A, Typical Characteristics

PKR 4628

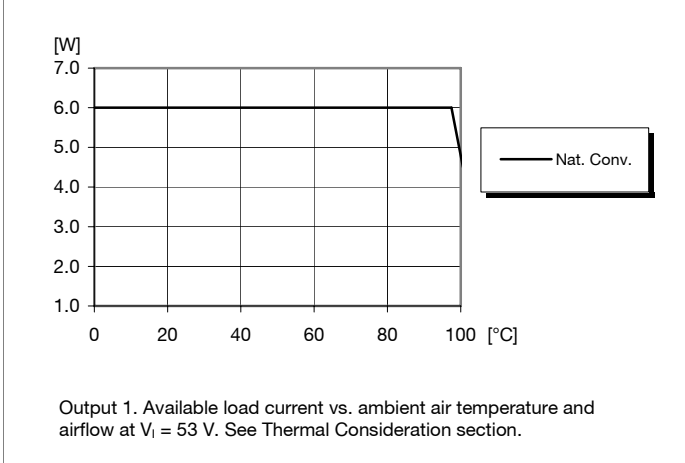
Efficiency



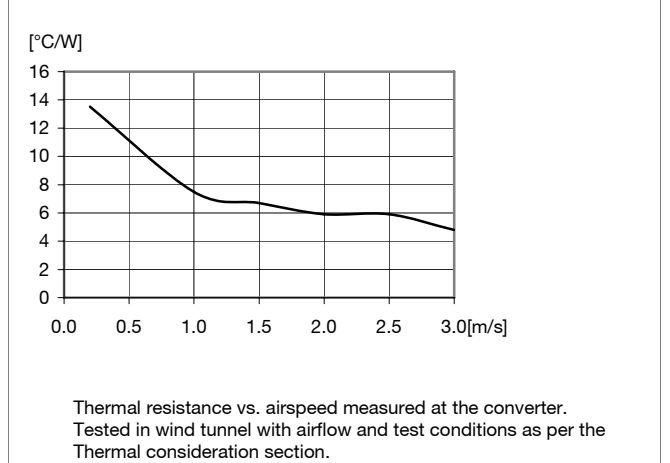
Power Dissipation



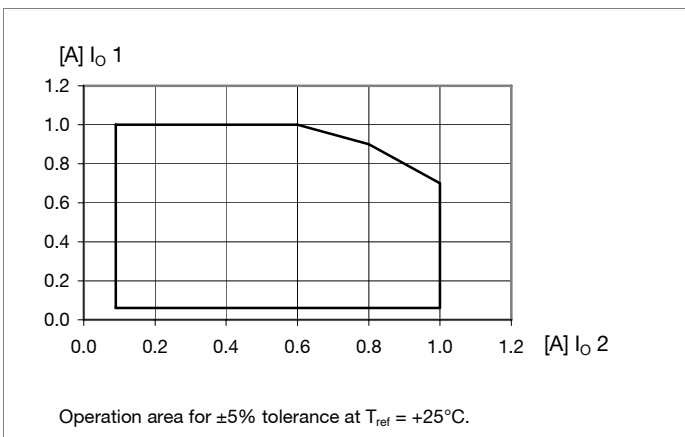
Output Power Derating



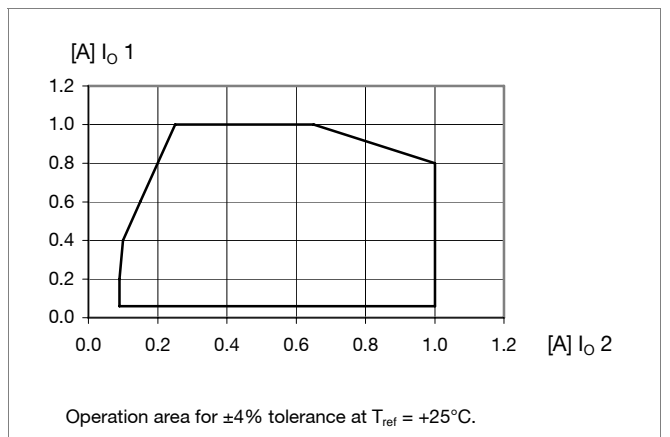
Thermal Resistance



Cross regulation output 1 (+5V)



Cross regulation output 2 (+3.3V)



PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

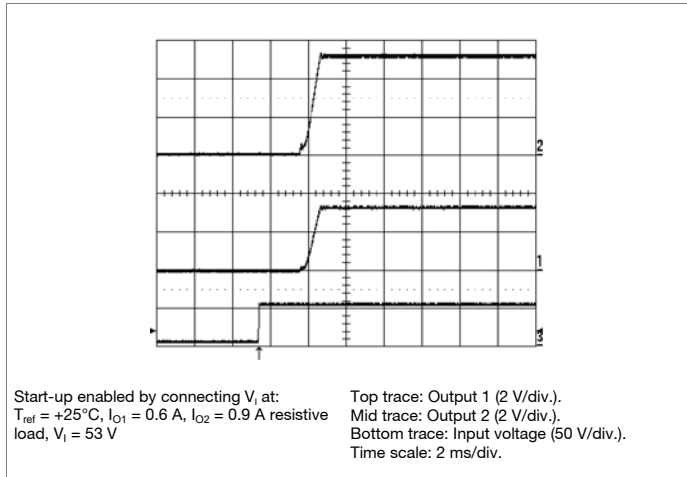
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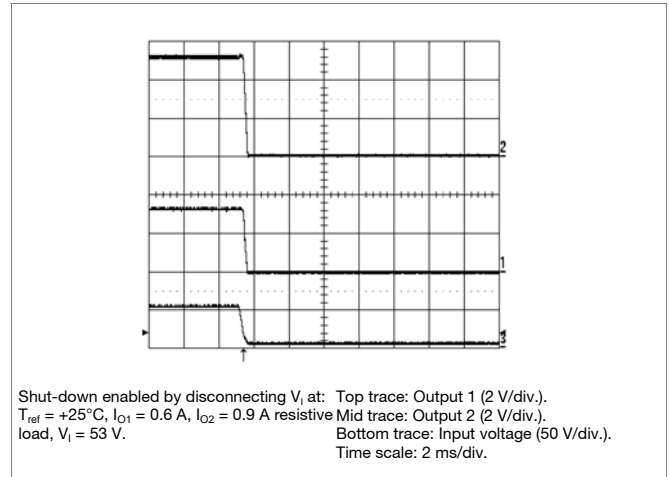
Dual 5 V/0.6 A, 3.3 V/0.9 A, Typical Characteristics

PKR 4628

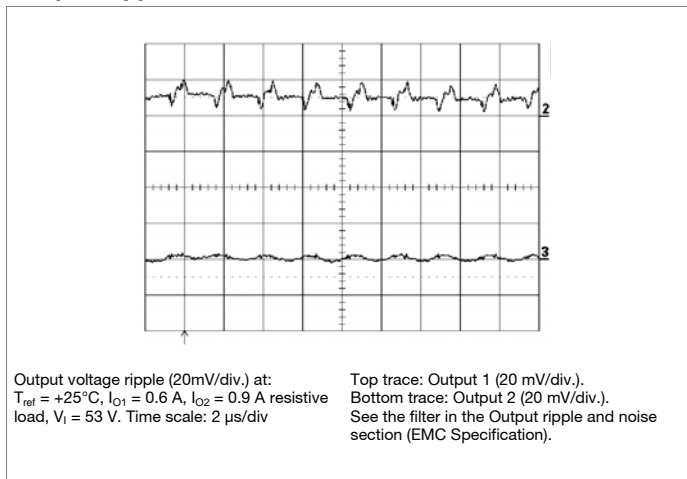
Start-up



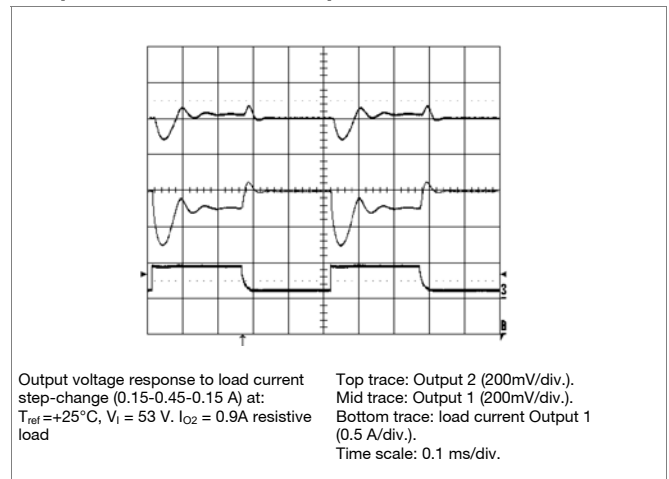
Shut-down



Output Ripple & Noise



Output Load Transient Response



Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{ou} = 0.495 \times (3.93 - V_o) / (V_o - 3.27) \text{ k}\Omega$$

E.g. Increase 4% => $V_o = 3.40\text{ Vdc}$

$$0.495 \times (3.93 - 3.4) / (3.4 - 3.27) = 2.02 \text{ k}\Omega$$

Output Voltage Adjust Downwards, Decrease:

$$R_{od} = 1.986 \times (3.27 - V_o) / (V_o - 2.59) \text{ k}\Omega$$

E.g. Decrease 2% => $V_o = 3.20\text{ Vdc}$

$$1.986 \times (3.27 - 3.20) / (3.20 - 2.59) = 0.228 \text{ k}\Omega$$

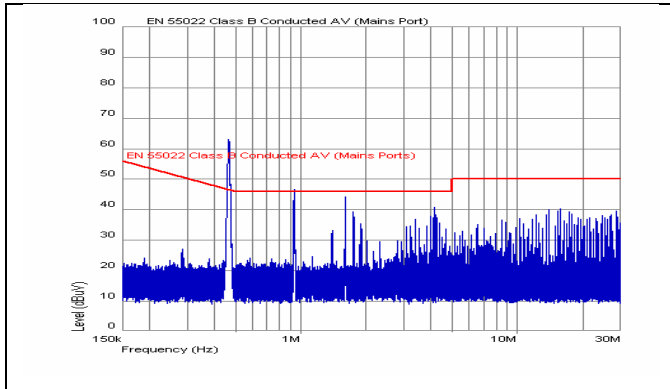
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EMC Specification

Conducted EMI measured according to EN55022, CISPR 22 and FCC part 15J (see test set-up).
The fundamental switching frequency is 485 kHz for PKR 4000 @ $V_I = 53$ V, max I_O .

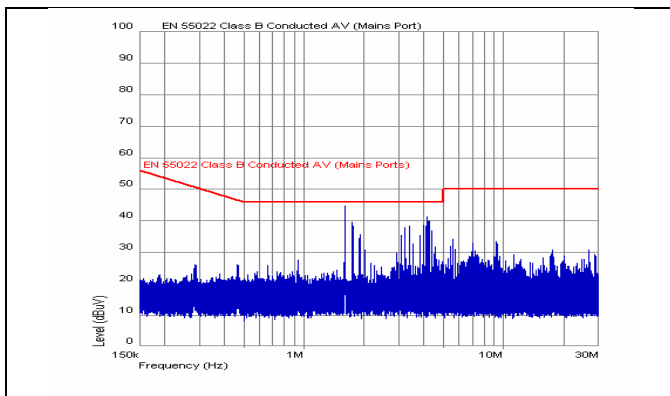
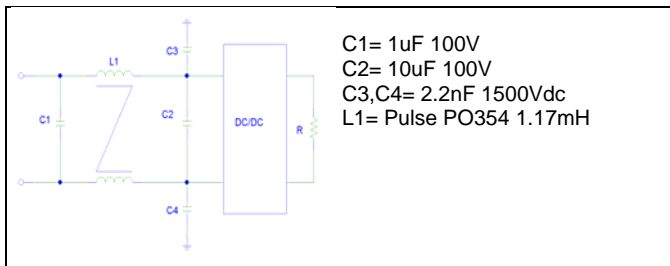
Conducted EMI Input terminal value (typ)



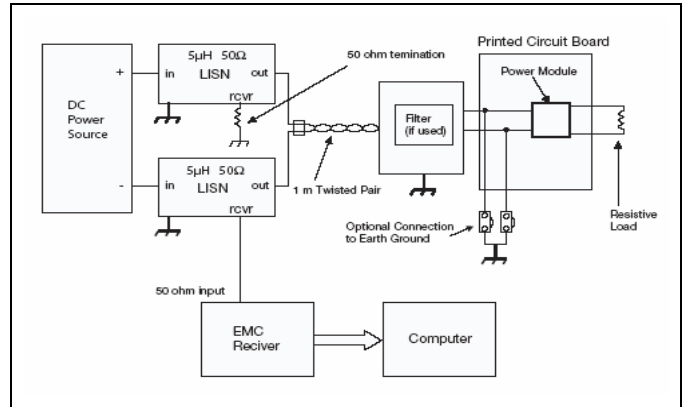
EMI without filter

External filter (class B)

Required external input filter in order to meet class B in EN 55022, CISPR 22 and FCC part 15J.



EMI with filter



Test set-up

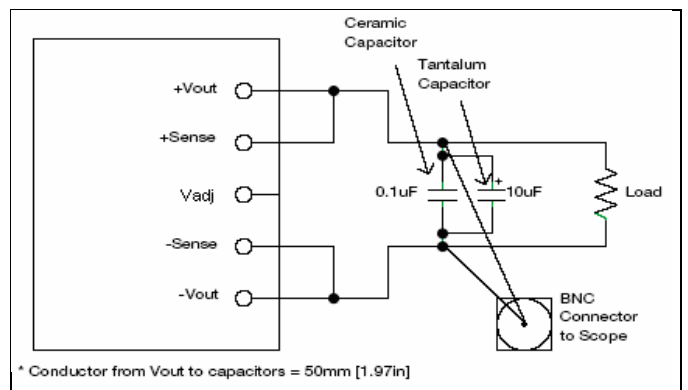
Layout recommendation

The radiated EMI performance of the DC/DC converter will depend on the PCB layout and ground layer design. It is also important to consider the stand-off of the DC/DC converter. If a ground layer is used, it should be connected to the output of the DC/DC converter and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PCB and improve the high frequency EMC performance.

Output ripple and noise

Output ripple and noise measured according to figure below.



Output ripple and noise test setup

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Operating information

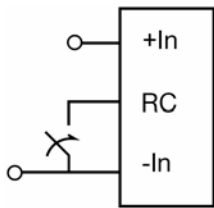
Input Voltage

The input voltage range 38...72Vdc meets the requirements of the European Telecom Standard ETS 300 132-2 for normal input voltage range in -48 and -60 Vdc systems, -40.5...-57.0 V and -50.0...-72 V respectively. At input voltages exceeding 72 V, the power loss will be higher than at normal input voltage and T_{ref} must be limited to absolute max +85°C. The absolute maximum continuous input voltage is 75Vdc.

Turn-off Input Voltage

The PKR 4000 Series DC/DC converters monitor the input voltage and will turn on and turn off at predetermined levels. The minimum hysteresis between turn on and turn off input voltage is 1V.

Remote Control (RC)



The products are fitted with a remote control function referenced to the primary negative input connection (- In), with positive logic. The RC function allows the converter to be turned on/off by an external device like a semiconductor or mechanical switch. The RC pin has an internal pull up resistor to + In.

The maximum required sink current is 0.1 mA. When the RC pin is left open, the voltage generated on the RC pin is <15 V. To ensure that the converter stays off the voltage must be below 1.0V.

Input voltage adjust

The converter has an under voltage monitor which make the converter turn-on and turn-off at predetermined input voltages level. These levels can be adjusted upwards or downwards by means of external resistors. To increase the voltage level, a resistor is connected between pin 11 and 17. The resistance value is given by the equation:

For $V_i > 36.5V$, $R_{iu_{nom}} = 100 \times (100.2 - V_i) / (V_i - V_{on})$, V_{on} is the turn-on level, which is typical 35.1V.

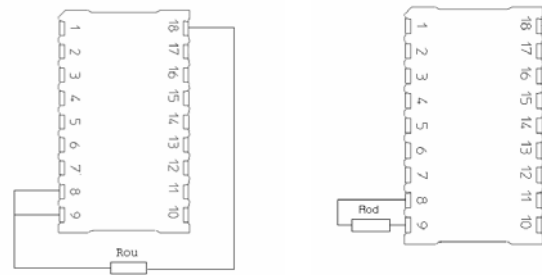
To decrease the voltage levels, a resistor is connected between pins 11 and 10. The resistor value is given by the equation. For $30 < V_i < 36.5V$,

$R_{in_{nom}} = 364 \times (V_i - 29.9) / (V_{on} - V_i)$, V_{on} is the turn-on level, which is typical 35.1V. V_i is the desired turn-on input voltage.

Output Voltage Adjust (V_{adj})

All PKR 4000 Series DC/DC converters have an Output Voltage Adjust pin (V_{adj}). This pin can be used to adjust the output voltage above or below output voltage initial setting. At increased output voltages the maximum power rating of the converter remains the same, and the max output current must be decreased correspondingly.

The output voltage can be increased or decreased by means of external resistors or other external circuitry. If other circuitry is used, the slew rate has to be limited to maximum 5 V/ms. To increase the voltage a resistor should be connected between the V_{adj} pin and +IN. To decrease the output voltage, a resistor is connected between V_{adj} pin and NOR pin. The resistor value of the Output voltage adjust function is according to information given under the Output section for the respective product.



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Operating information, cont.

Input And Output Impedance

The impedance of both the input source and the load will interact with the impedance of the DC/DC converter. It is important that the input source has low characteristic impedance. The PKR 4000 Series DC/DC converters are designed for stable operation without external capacitors connected to the input or output. The performance in some applications can be enhanced by addition of external capacitance as described under maximum capacitive load. If the input voltage source contains significant inductance, the addition of a 10 μ F capacitor across the input of the converter will ensure stable operation. The capacitor is not required when powering the DC/DC converter from an input source with an inductance below 10 μ H.

Maximum Capacitive Load

When powering loads with significant dynamic current requirements, the voltage regulation at the load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic capacitors as close to the load as possible, using several capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while electrolytic capacitors should be used to handle low frequency dynamic load changes.

It is equally important to use low resistance and low inductance PCB layouts and cabling.

External decoupling capacitors will become part of the control loop of the DC/DC converter and may affect the stability margins. As a "rule of thumb", 100 μ F/A of output current can be added without any additional analysis. The absolute maximum value of output capacitance is 5 000 μ F. For further information please contact your local Ericsson Power Modules representative.

Parallel Operation

Paralleling of several PKR 4000 converters is easily accomplished by direct connection of the output voltage terminal pins. The load regulation characteristic is specifically designed for optimal paralleling performance. Load sharing between converters will be within $\pm 10\%$. It is recommended not to exceed $P_O = n \times 0.9 \times P_{Omax}$, where P_{Omax} is the maximum converter output power and n the number of paralleled converters, to prevent overloading any of the converters and thereby decreasing the reliability performance.

Over Current Protection (OCP)

The PKR 4000 Series DC/DC converters include current limiting circuitry for protection at continuous overload. The output voltage will decrease towards zero for output currents in excess of max output current (max I_O). The converter will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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Thermal Consideration

General

The PKR 4000 series DC/DC converters are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation. Cooling is achieved mainly by conduction, from the pins to the PCB board, and convection, which is dependant on the airflow across the converter. Increased airflow enhances the cooling of the converter.

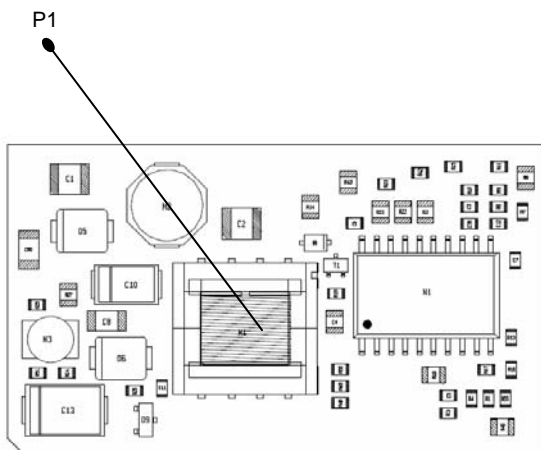
The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at $V_{in} = 53V$.

The DC/DC converter is tested on a 254 x 254 mm, 35 μm (1 oz), 8-layer test board mounted vertically in a wind tunnel with a cross-section of 305 x 305 mm.

Proper cooling of the DC/DC converter can be verified by measuring the temperature at positions P1. The temperature at this position should not exceed the max values provided in the table below.

Note that the max value is the absolute maximum rating (non destruction) and that the electrical Output data is guaranteed up to $T_{ref} + 85^{\circ}C$.

Position	Device	Designation	max value
P ₁	Transformer	T _{ref}	110° C



Ambient Temperature Calculation

By using the thermal resistance the maximum allowed ambient temperature can be calculated.

1. The power loss is calculated by using the formula $((1/\eta) - 1) \times \text{output power} = \text{power losses (Pd)}$.
 η = efficiency of converter. E.g 85 % = 0.85
2. Find the thermal resistance (Rth) in the Thermal Resistance graph found in the Output section for each model. Calculate the temperature increase (ΔT).
 $\Delta T = Rth \times Pd$
3. Max allowed ambient temperature is:
 $\text{Max Tref} - \Delta T$.

E.g PKR 4713 SI at 1m/s:

1. $((\frac{1}{0.85}) - 1) \times 7 W = 1.24 W$
2. $1.24 W \times 14.0^{\circ}C/W = 17.3^{\circ}C$
3. $110^{\circ}C - 17.3^{\circ}C = \text{max ambient temperature is } 92.7^{\circ}C$

The actual temperature will be dependent on several factors such as the PCB size, number of layers and direction of airflow.

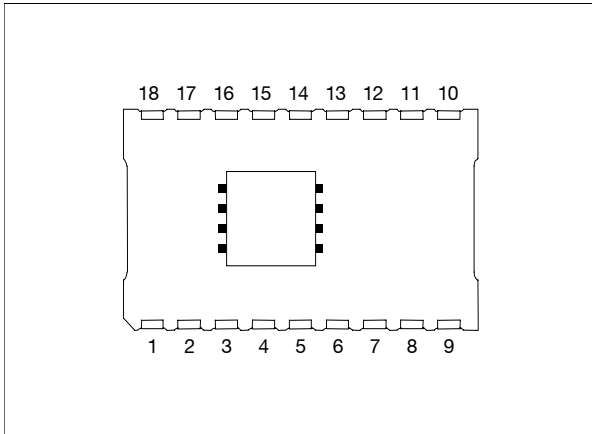
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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Connections

Top view with pin numbers

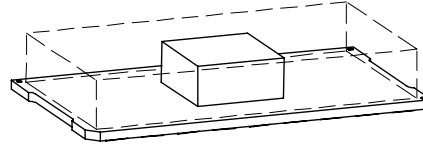
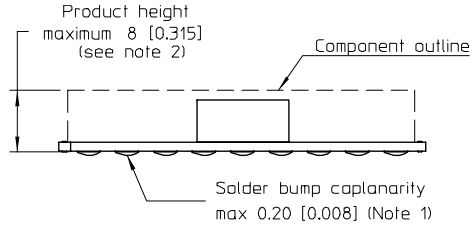


Pin	Designation	Function
1	Out 1	Output 1
2	Rtn	Output return
3	Out 2	Output 2
4	NC	Not connected
5	NC	Not connected
6	NC	Not connected
7	NC	Not connected
8	Vadj	Output voltage adjust
9	NOR	Connection of Nominal Output voltage Resistor
10	TOA	Turn-on/off input voltage adjust
11	RC	Remote control and turn-on/off input voltage adjust
12	NC	Not connected
13	NC	Not connected
14	NC	Not connected
15	NC	Not connected
16	NC	Not connected
17	- In	Negative Input
18	+In	Positive input

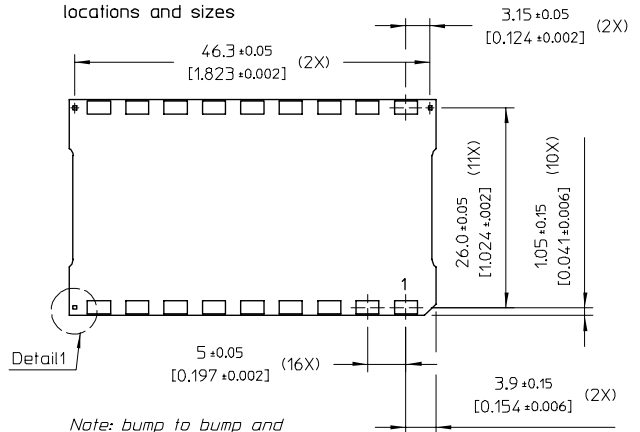
PKR 4000 series
DC/DC converters, Input 36-75 V, Output 1.5 A/7 W

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Mechanical Information – Surface Mount Version

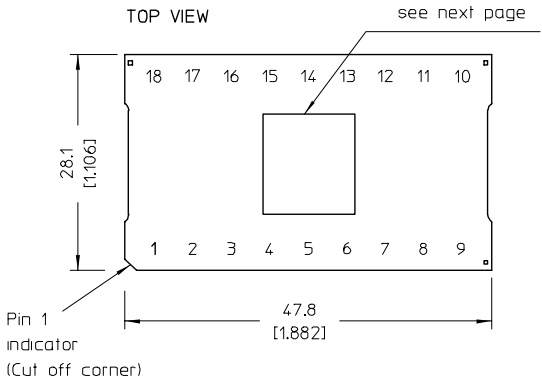


BOTTOM VIEW
Solder bump and fiducial mark locations and sizes

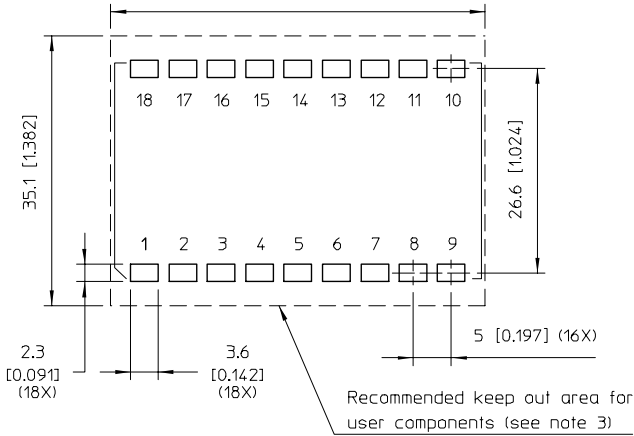


Note: bump to bump and fiducial to bump tolerances are not cumulative

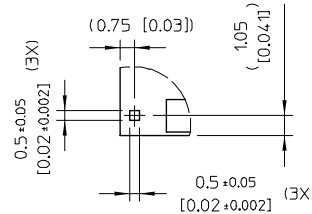
TOP VIEW
Pick-up surface, see next page



FOOTPRINT (top view, application board)
48.8 [1.921]



Detail1
Bottom side fiducial marks



For detail of bottom side solder bumps, see next page

Fiducial mark finish is Ni/Au coated Cu

NOTES

- The solder bumps are designed to allow coplanarity compensation by melting of the solder bumps between the product and the application board. The coplanarity corresponds to the requirements for BGA low melt solder balls. (Jedec Publication 95, Design Guide 4.14 revision E, september 2005)
- Max product height is measured from bottom side of the product PCB but excluding the solder bump (reduced to solder joint thickness after assembly)
- Absolute keep out area = 48,8 x 29,1 based on mechanical outline and assembly tolerances. The recommended keep out area is +3 mm on each long side to facilitate repair (removal and re-mounting) with a hot air nozzle.

Weight: 9-12 g
All dimensions in mm [inch]
Tolerances unless specified
x.x mm ±0.26 [0.01], x.xx mm ±0.13 [0.005]
(not applied on footprint or typical values)

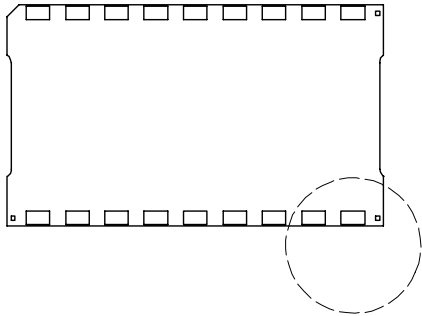


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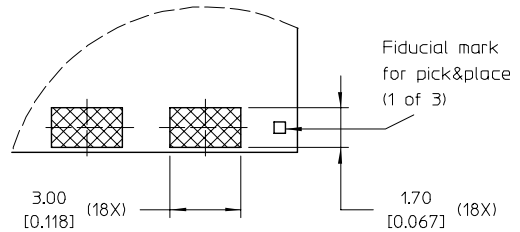
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Assembly Information – Surface Mount Version

BOTTOM VIEW

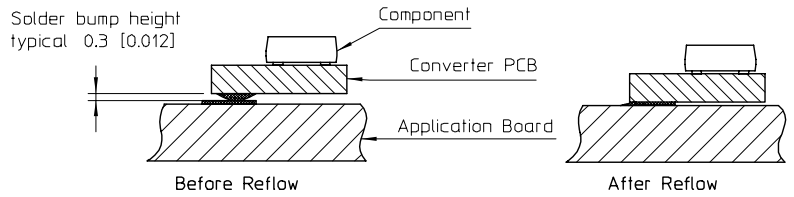


Detail
Bottom side solder bumps



Solder bump composition: no-clean SAC solder,
approximate composition Sn96/Ag3/Cu1, melting range 217–221°C

APPLICATION VIEW (detail)



ALTERNATIVE FOOTPRINT, equal to the recommended PKF footprint (top view, application board)

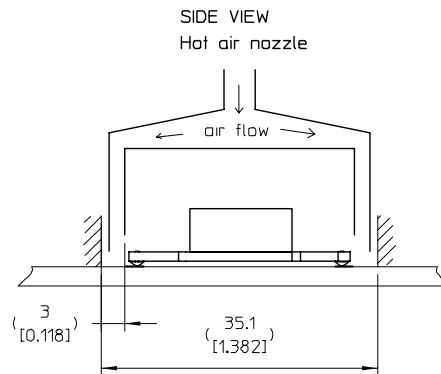
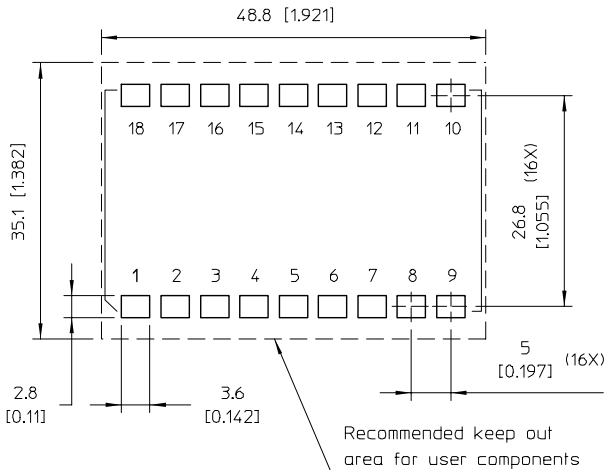


Illustration of a recommended design for a hot air repair nozzle for manual removal and re-mounting

The recommended footprint (see previous page) is optimised for the solder bump design. However, the standard PKF footprint will also accommodate this solder bump design. The only differences is the solder pad width (2.8 versus 2.3 mm) and the c-c distance between the two rows of connectors (26.8 versus 26.6 mm).

The absolute and recommended keep out areas are not affected by the differences in application board footprint.

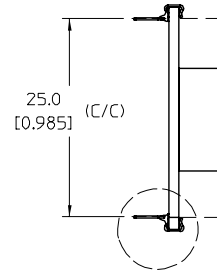
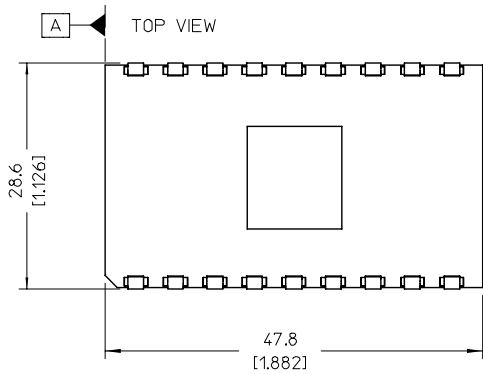
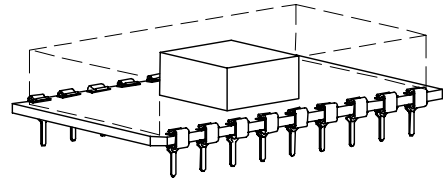
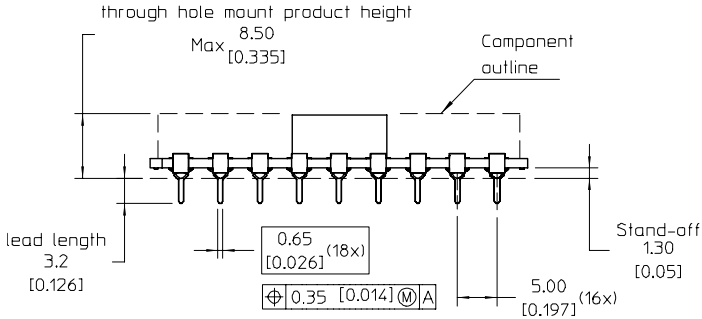
Weight: 9-12 g
All dimensions in mm [inch]
Tolerances unless specified
x.x mm ±0.26 [0.01], x.xx mm ±0.13 [0.005]
(not applied on footprint or typical values)



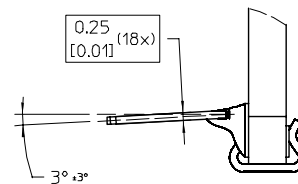
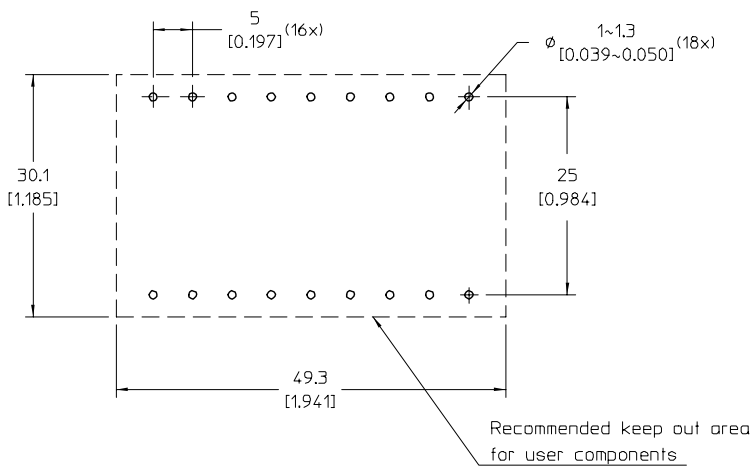
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Mechanical Information – Through Hole Mount Version

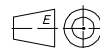


RECOMMENDED FOOTPRINT
(top view, application board)



PIN SPECIFICATION
Material: CuSn6 (C5191)
Plating: 3 to 5 μm matte Sn
over minimum 1.5 μm Ni

Weight: typical 11 g
All dimensions in mm [inch]
Tolerances unless specified
x.x mm ±0.26 [0.01], x.xx mm ±0.13 [0.005]
(not applied on footprint or typical values)



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Soldering Information - Surface Mounting

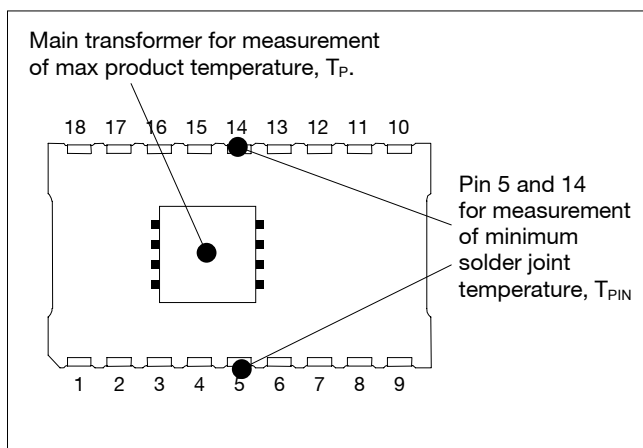
The surface mount version of the product is intended for convection or vapor phase reflow in SnPb and Pb-free processes. To achieve a good and reliable soldering result, make sure to follow the recommendations from the solder paste supplier, to use state-of-the-art reflow equipment and reflow profiling techniques as well as the following guidelines.

When using and Sn/Pb solder in the host board assembly process, the composition of the mix of Pb- and Pb-free solder results in a higher melting temperature compared to standard Pb solder.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board. The cleaning residues may affect long time reliability and isolation voltage.

Minimum Pin Temperature Recommendations

Pin number 5 and 14 are chosen as reference locations for the minimum pin temperature recommendations since these will likely be the coolest solder joints during reflow.



SnPb solder processes

For Pb solder processes, a pin temperature (T_{PIN}) in excess of the solder melting temperature, (T_L , +183°C for Sn63/Pb37) for more than 30 seconds, and a peak temperature of +210°C is recommended to ensure a reliable solder joint.

Lead-free (Pb-free) solder processes

For Pb-free solder processes, a pin temperature (T_{PIN}) in excess of the solder melting temperature (T_L , +217 to +221 °C for Sn/Ag/Cu solder alloys) for more than 30 seconds, and a peak temperature of +235°C on all solder joints is recommended to ensure a reliable solder joint.

Peak Product Temperature Requirements

The main transformer is chosen as reference location for the maximum (peak) allowed product temperature (T_p), since this will likely be the warmest part of the product during the reflow process.

To avoid damage or performance degradation of the product, the reflow profile should be optimized to avoid excessive heating. A sufficiently extended preheat time is recommended to ensure an even temperature across the host PCB, for both small and large devices. To reduce the risk of excessive heating is also recommended to reduce the time in the reflow zone as much as possible.

SnPb solder processes

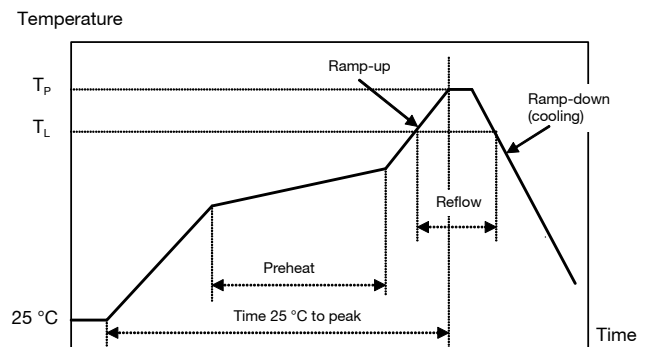
For conventional SnPb solder processes, the product is qualified for MSL 1 according to IPC/JEDEC standard J-STD-020C.

During reflow, T_p must not exceed +225 °C at any time.

Lead-free (Pb-free) solder processes

For Pb-free solder processes, the product is qualified for MSL 3 according to IPC/JEDEC standard J-STD-020C.

During reflow, T_p must not exceed +260 °C at any time.



Reflow process specifications		Sn/Pb eutectic	Pb-free
Average ramp-up rate		3°C/s max	3°C/s max
Solder melting temperature (typical)	T_L	+183°C	+221°C
Minimum time above T_L		30 s	30 s
Minimum pin temperature	T_{PIN}	+210°C	+235°C
Peak product temperature	T_p	+225°C	+260°C
Average ramp-down rate		6°C/s max	6°C/s max
Time 25°C to peak		6 minutes max	8 minutes max

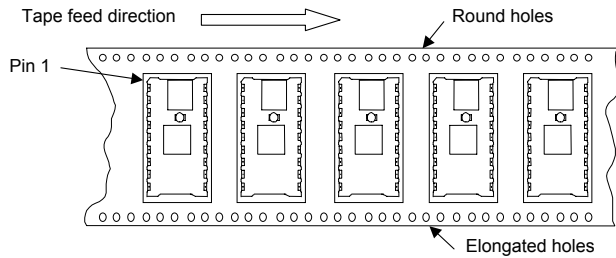
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Delivery Package Information – SM version

The surface mount version of the product is delivered in antistatic injection molded trays (Jedec design guide 4.10D standard) or in antistatic carrier tape (EIA 481 standard)

Carrier Tape Specifications	
Material	Polystyrene (PS), antistatic
Surface resistivity	< 10 ⁷ Ohm/square
Bakeability	The tape is not bakable
Tape width	72 mm [2.835] inch]
Pocket pitch	36 mm [1.417 inch]
Pocket depth	9.2 mm [0.362 inch]
Reel diameter	330 mm [13 inch]
Reel capacity	150 products /reel
Reel weight	Approximately 2.5 kg/full reel



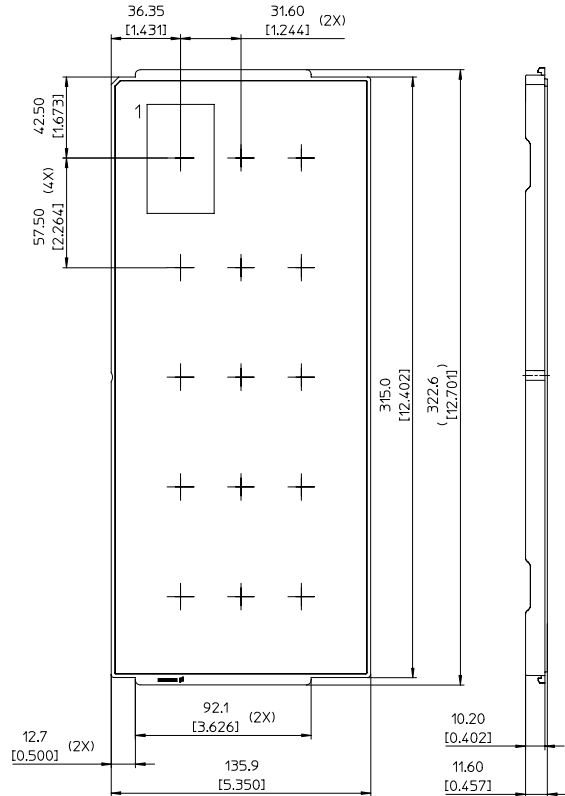
Tray Specifications	
Material	PPE, antistatic
Surface resistance	10 ⁵ < Ohm/square < 10 ¹²
Bakeability	The trays can be baked at maximum 125 °C for maximum 48 hours
Tray capacity	15 products/tray
Box capacity	150 products (10 full trays/box)
Tray weight	140 g empty, 320 g full tray maximum



Dry Pack Information

The surface mount version of the product is delivered in trays or tape & reel. These inner shipment containers are dry packed in standard moisture barrier bags according to IPC/JEDEC standard J-STD-033 (Handling, packing, shipping and use of moisture/reflow sensitivity surface mount devices).

Using products in high temperature Pb-free soldering processes requires dry pack storage and handling. In case the products have been stored in an uncontrolled environment and no longer can be considered dry, the modules must be baked according to J-STD-033.

NOTE: tray dimensions refer to pocket center.
Exact position of pick-up point depends on the position of the main transformer of the individual product variant



  All dimensions in mm [inch]
Tolerances: x.x mm ±0.13 [0.005], x.xx mm [±0.26 [0.01]

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Soldering Information – Through Hole Mounting

The through hole mount version of the product is intended for manual or wave soldering. When wave soldering is used, the temperature on the pins is specified to maximum 270 °C for maximum 10 seconds. A maximum preheat rate of 4°C/s and a temperature of max of +150°C is suggested.

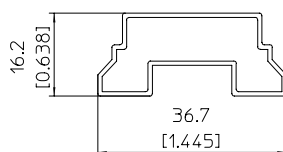
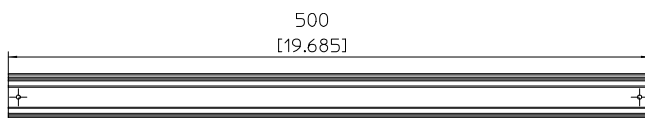
When soldering by hand, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to prevent overheating.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board. The cleaning residues may affect long time reliability and isolation voltage.

Delivery Package Information – TH version

The through hole mounted version of the product is delivered in antistatic tubes.

Tube Specifications	
Material	PVC, transparent with antistatic coating
Surface resistance	< 10 ¹¹ Ohm/square
Bakability	The tubes are not bakable.
Tube capacity	10 products/tube
Box capacity	100 products (10 full tubes/box)
Tube weight	Typical 160 g full tube



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Product Qualification Specification

Characteristics			
External visual inspection	IPC-A-610		
Operational life test	MIL-STD-202G method 108A With power cycling	T _{ref} Load Duration	According to Absolute maximum ratings Maximum output power 500 h
Vibration, broad band random	IEC 60068-2-64 Fh	Frequency Acceleration spectral density Duration and directions	10 to 500 Hz 0.5 g ² /Hz 10 min in each 3 perpendicular directions
Vibration, sinusoidal	IEC 68-2-64 F _c	Frequency Amplitude Acceleration Sweep rate Duration	10 to 500 Hz 0.75 mm 10 g 1 octave/min 2 h in each 3 perpendicular directions
Mechanical shock	IEC 68-2-27 E _a	Peak acceleration Duration Pulse shape Directions Number of pulses	100 g 6 ms Half sine 6 18 (3 + 3 in each perpendicular direction)
Change of temperature (Temperature cycling)	IEC 60068-2-14 Na	Temperature range Number of cycles Dwell time	-40 to +100°C 300 30 min
Robustness of terminations	IEC 68-2-21 U _{e1} IEC 68-2-21 U _{a1} IEC 68-2-21 U _b (5.2b)	Surface mount products Through hole mount products	All leads All leads
Solderability <i>SM version</i>	IEC 68-2-58 T _d	Temperature, SnPb Eutectic Temperature, Pb free Preconditioning	215 ±5°C 245 ±5°C 240 h in 85°C/85%RH
Solderability <i>TH version</i>	IEC 68-2-58 T _a	Temperature, Pb free Solder immersion time Preconditioning	260 ±5°C 5 ±0.5 s Steam ageing 8 h±15 minutes
Damp heat	IEC 60068-2-67 Cy with bias	Temperature Humidity Duration Preconditioning	+85 °C 85 % RH 500 hours Reflowed 3X according to IPC/JEDEC J-STD-020C MSL3 at 260°C
Moisture reflow sensitivity classification	J-STD-020C	SnPb Eutectic Pb free	MSL 1, peak reflow at 225°C MSL 3, peak reflow at 260°C
Immersion in cleaning solvents	IEC 68-2-45 XA Method 2	Water Isopropyl alcohol Glycol ether	+55 ±5°C +35 ±5°C +35 ±5°C
Cold (in operation)	IEC 68-2-1 A _d	Temperature T _A Duration	-40°C 72 h